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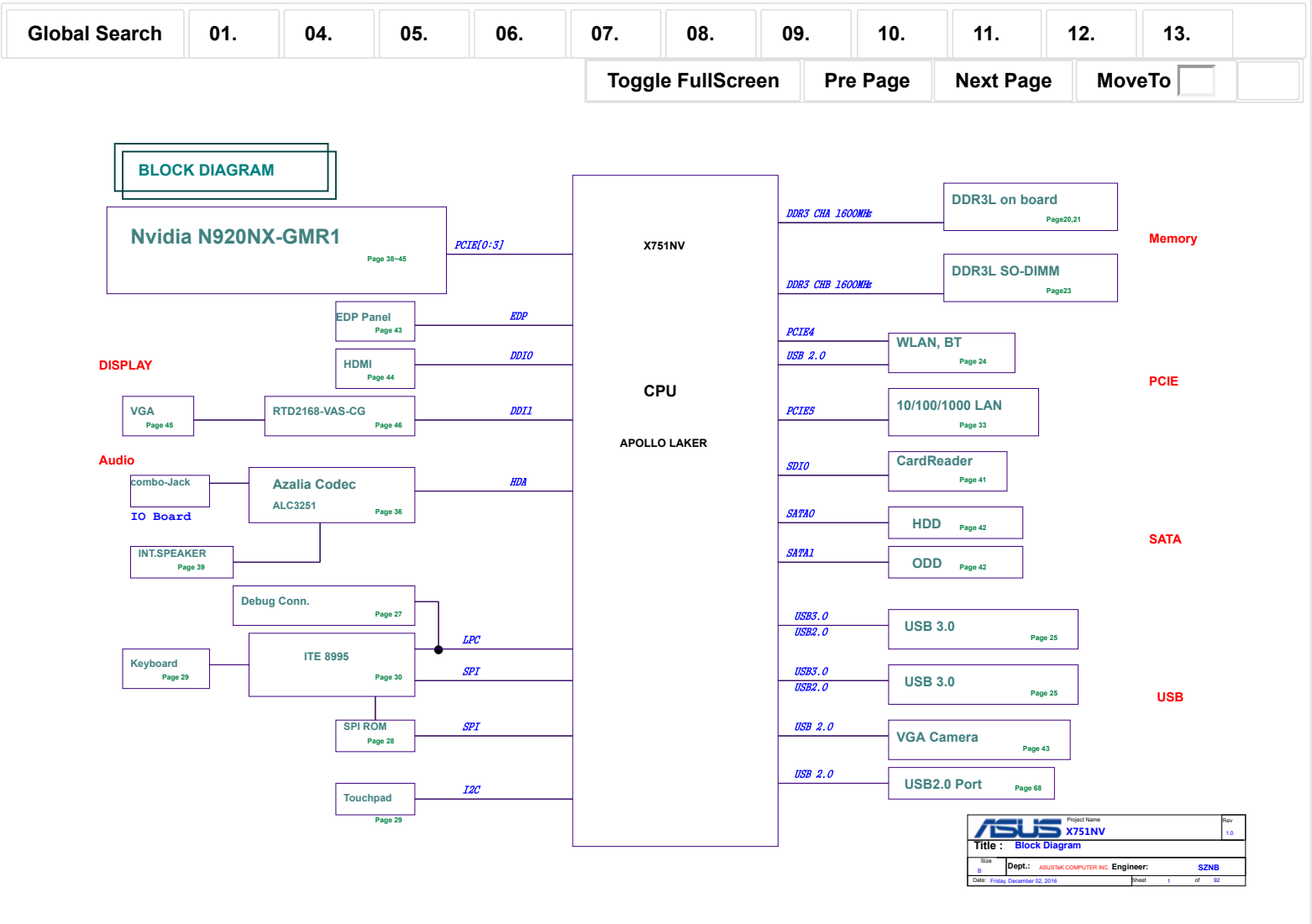
20.Memory down

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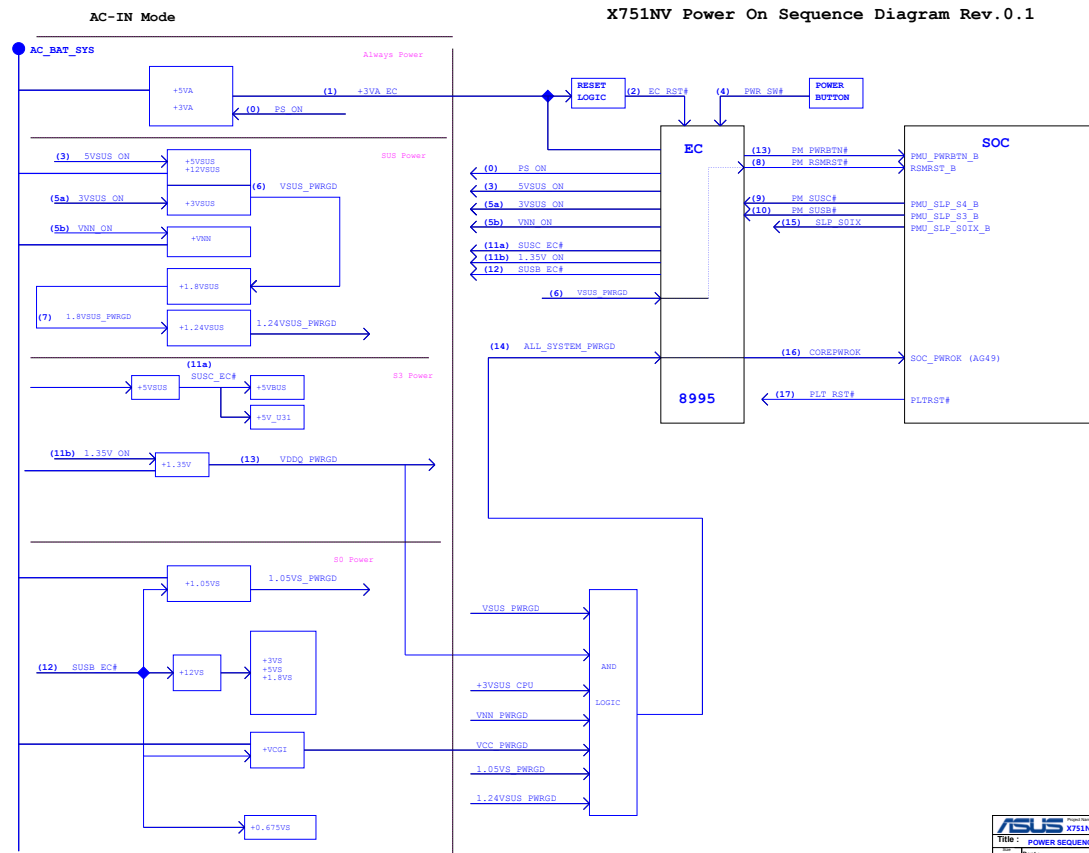
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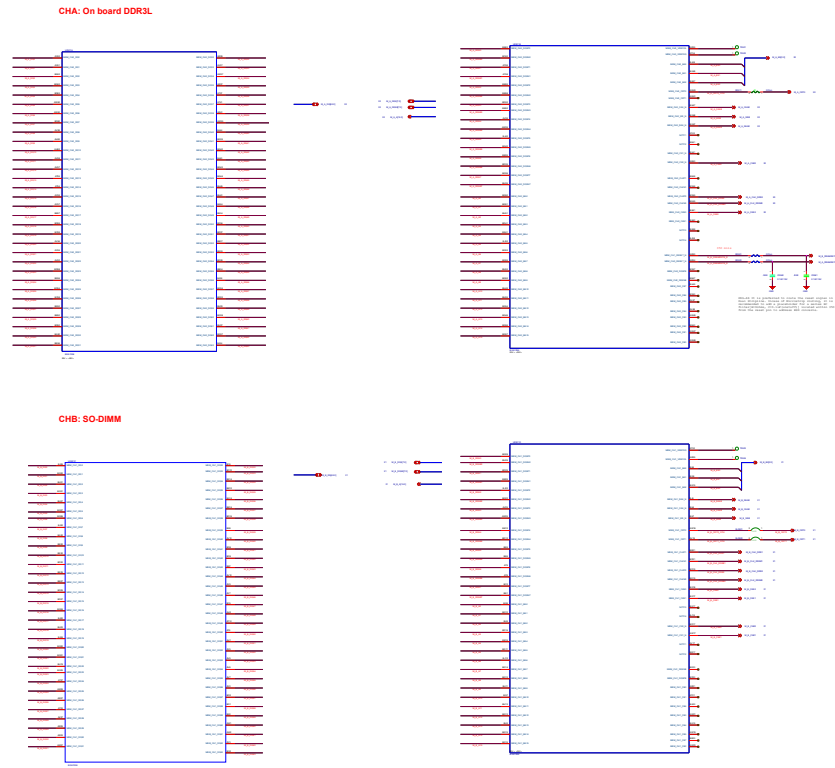
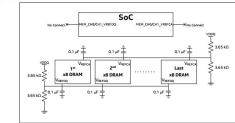


Figure 8: Memory Down Voltage and VDDQ Generation Example Circuit



NOTES:
1. Each channel must have its own circuit.
2. The memory must have a 0% tolerance.
3. SoC VDDQ output must be left floating for DDR3L Memory Down design.

Figure 9: Memory Down Voltage and VDDQ Generation Example Circuit



NOTES:
1. Each channel must have its own circuit.
2. The memory must have a 0% tolerance.
3. SoC VDDQ output must be left floating for DDR3L Memory Down design.

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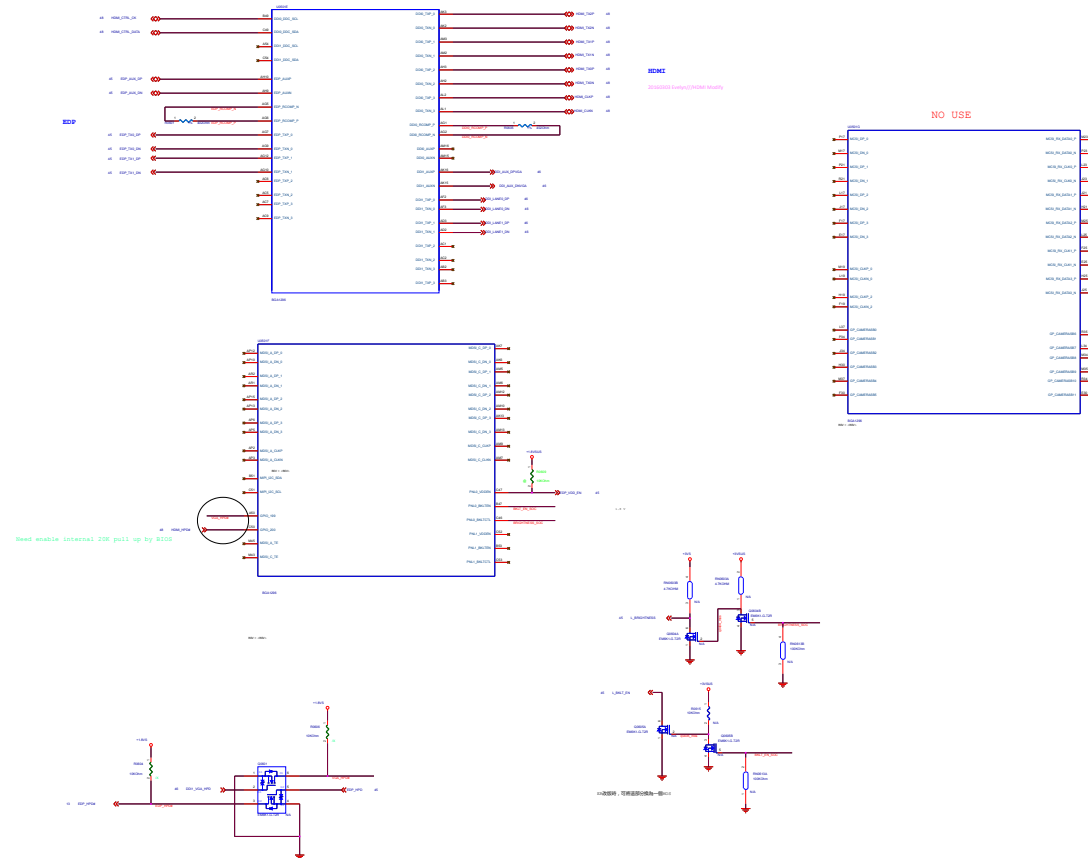
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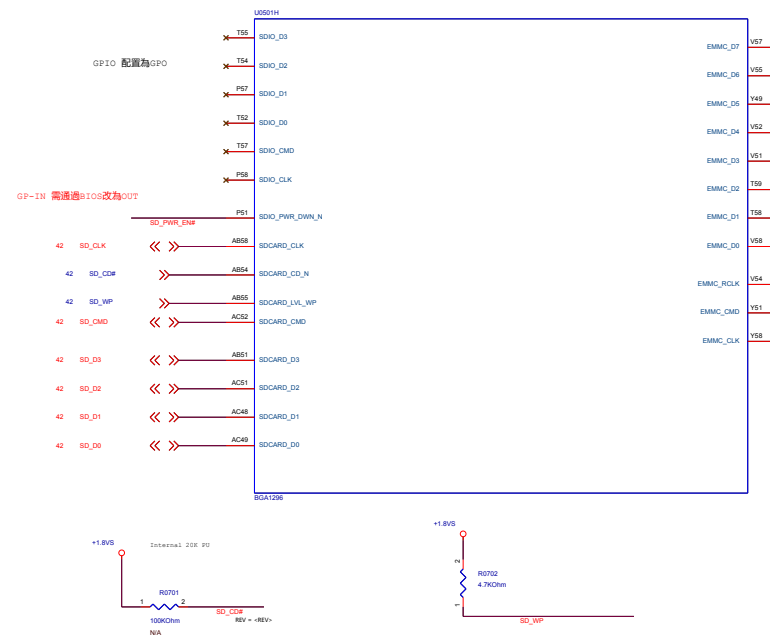
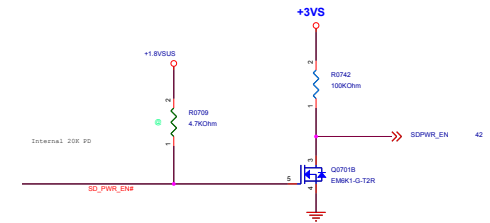
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ASUS		Project Name	Rev
X751NV			1.0
Title : CPU_SATA&PCIE&SPI&HDA			
Size	Dept.:	ASUSTeK COMPUTER INC. Engineer:	SZNB
Date: Friday, December 01, 2018		Sheet	7 of 82

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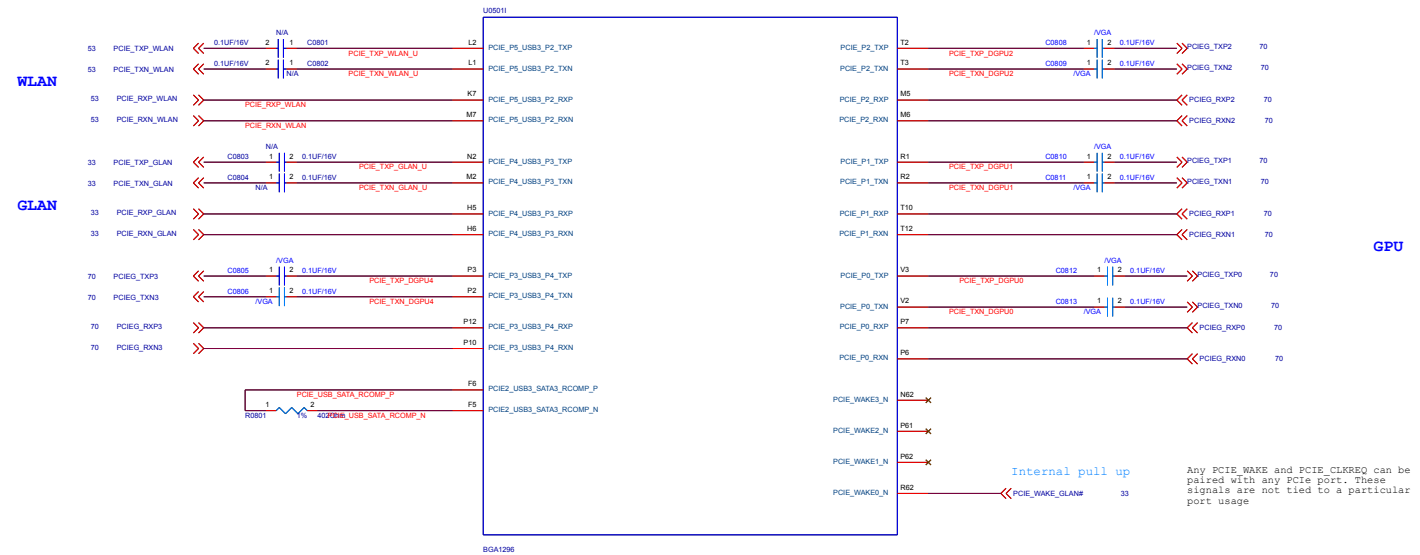
20.Memory down

21. So-Dimm

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23. CPU_PCH_CLK_GEN

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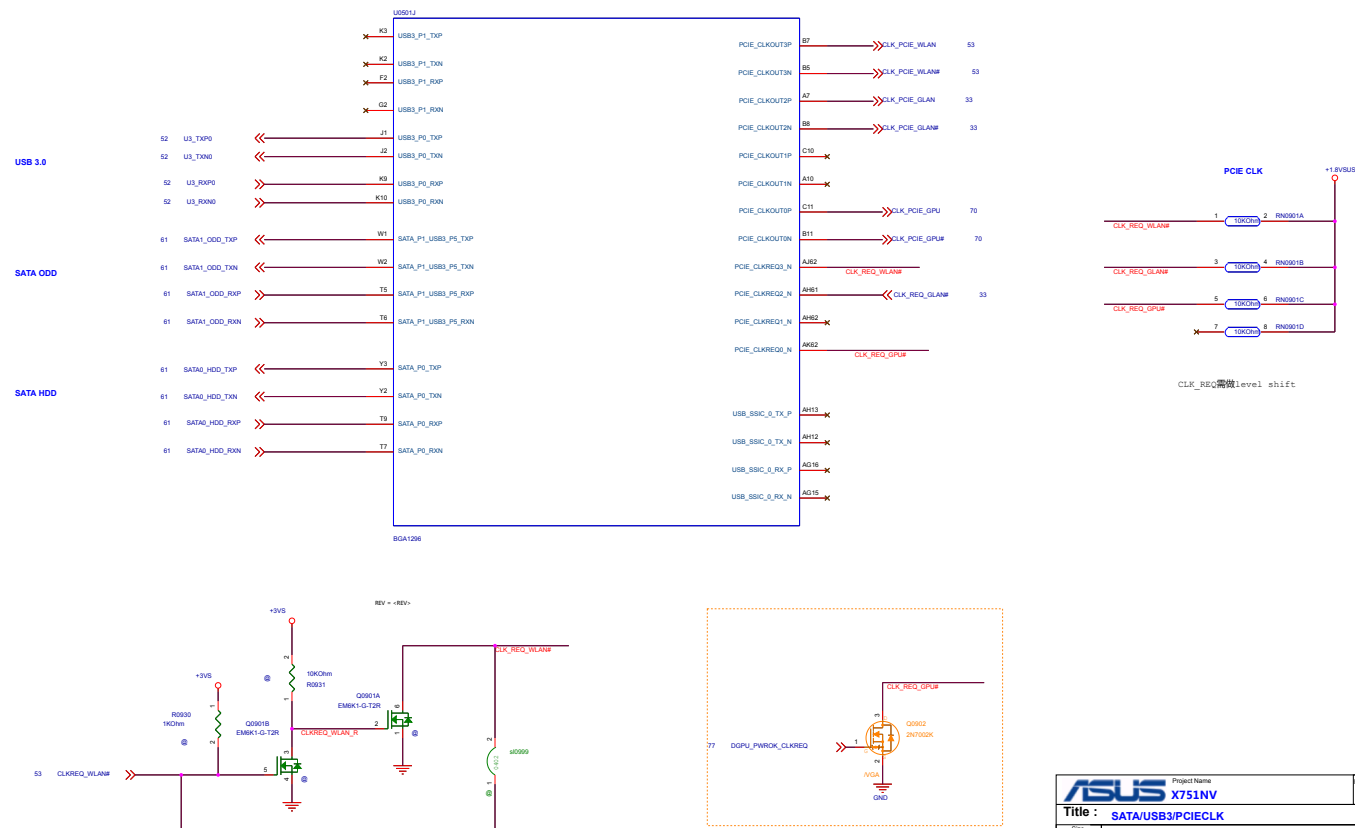
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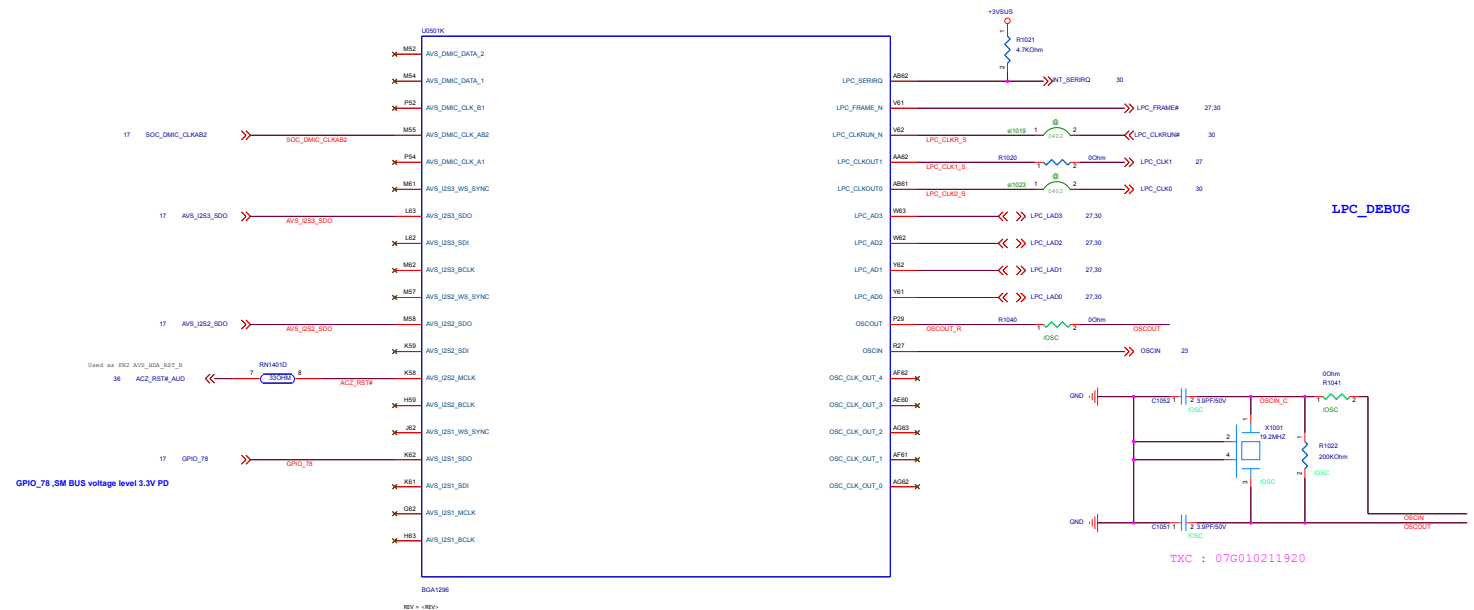
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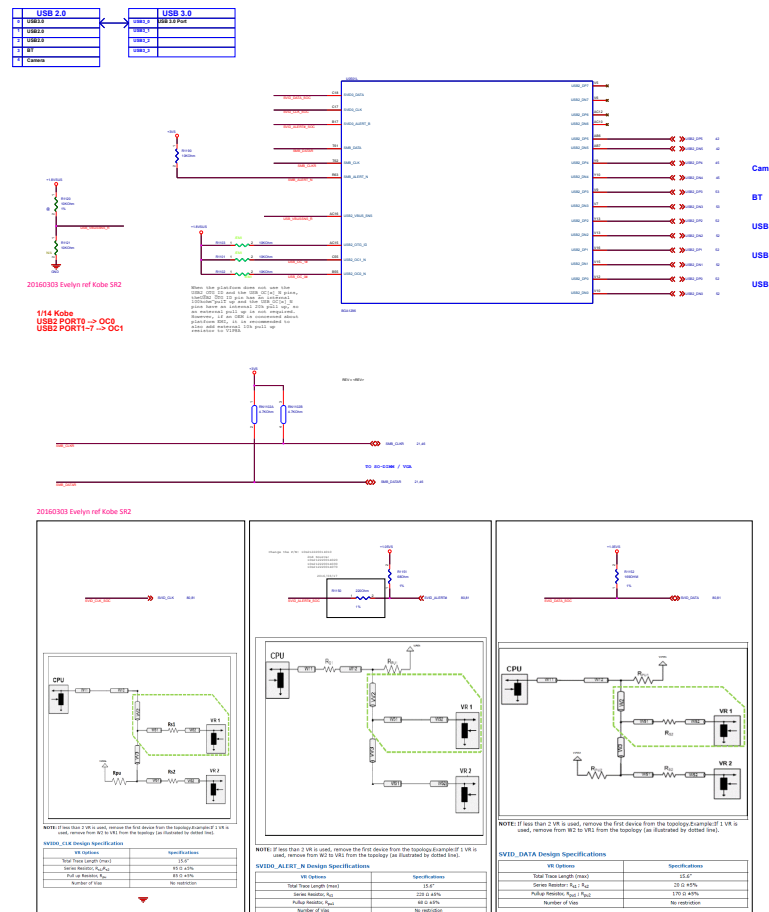
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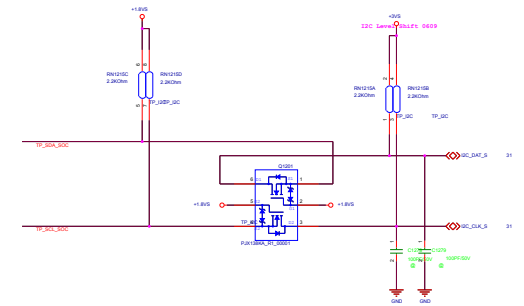
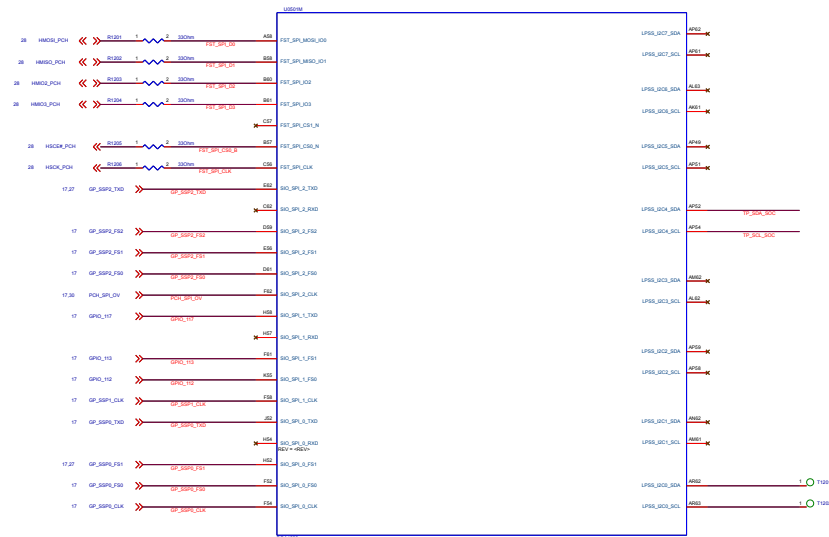
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Intel HDA #560733 remove after QS
E402NA remove at PR

HD Audio

编辑:CPU_PCH_CLK_GEN 0-10 通过添加时钟源 0-10 后一并, 并添加时钟

DRAM Strip

DRAM0_0A	DRAM0_0B	DRAM0_0C	DRAM0_0D	DRAM0_0E	DRAM0_0F
0	0	0	0	0	0
1	1	1	1	1	1
2	2	2	2	2	2
3	3	3	3	3	3
4	4	4	4	4	4
5	5	5	5	5	5
6	6	6	6	6	6
7	7	7	7	7	7
8	8	8	8	8	8
9	9	9	9	9	9

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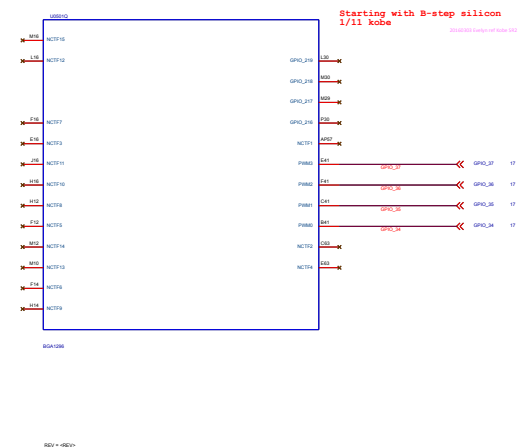
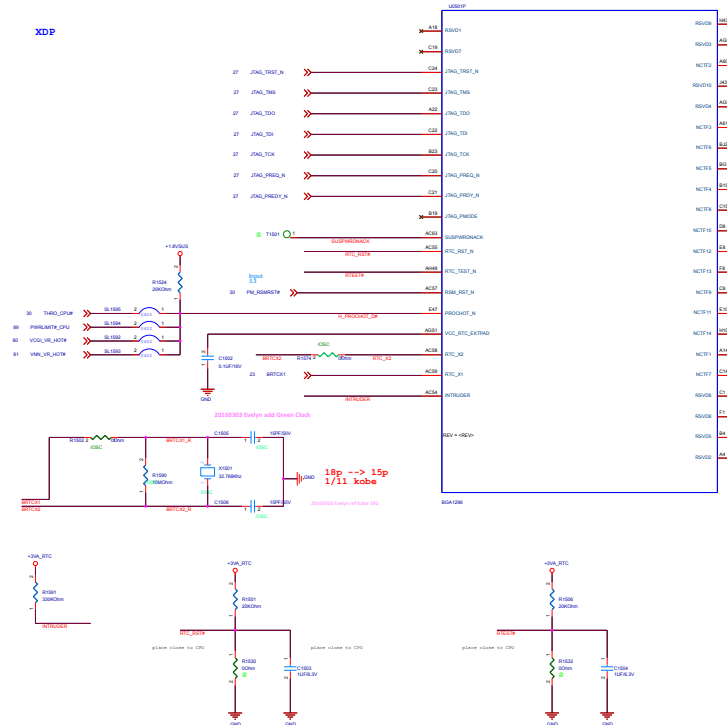
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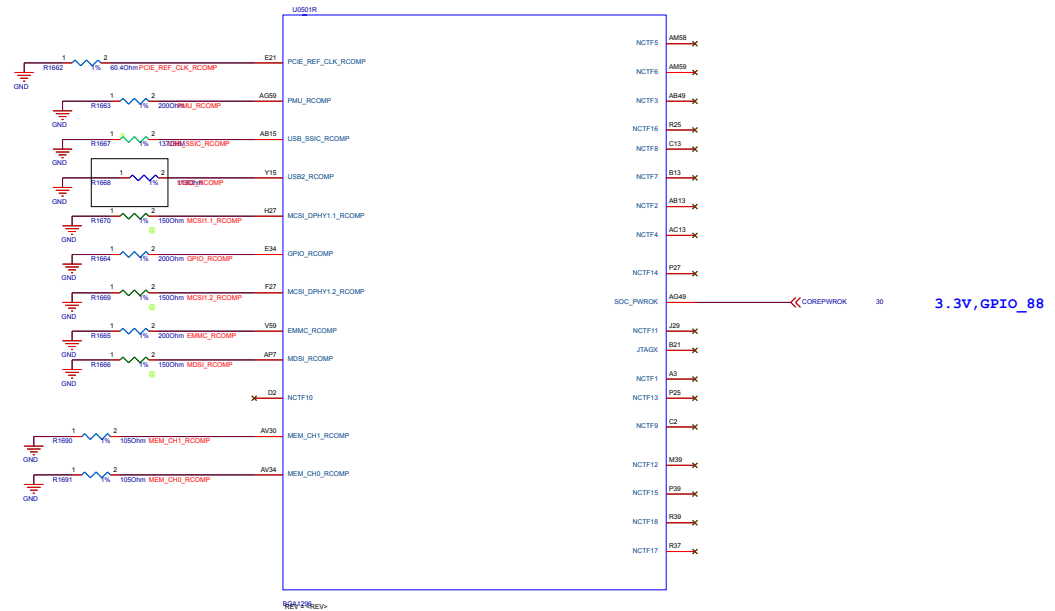
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Change the P/N: 100212113014070
2nd Source: 100212113014010
100212113014030
100212113014050 2016/03/17

If both MIPI-CSI and MIPI-DSI
interfaces are not used on the
platform, then the RCOMPs for
MIPI-CSI and MIPI-DSI can be left
unconnected on the platform.



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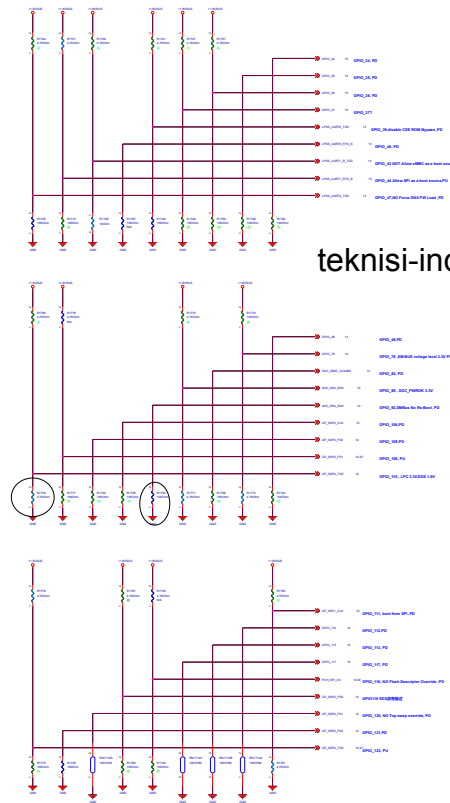
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CPU Strap



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Table 2-36. Hardware Straps

GPIO #	Purpose	Internal Termination	Pin Strap Usage/Description/Polarity
GPIO_34	RSVD	20K PD	Please ensure that this strap is always pulled low for normal platform operation.
GPIO_35	RSVD	20K PD	Please ensure that this strap is always pulled low for normal platform operation.
GPIO_36	VCC_LP04V, LP35V_A voltage selection	20K PD	1 = 1.35V 0 = 1.24V (default) Note: This strap will only be used for B-step. For A-step this rail should only be set at 1.24V
GPIO_39	Enable CSE ROM Bypass	20K PD	1 = enable bypass 0 = disable bypass (default) Note: Apollo Lake supports TXE3.0 (this is also called CSE). This strap tells CSE (TXE3.0) to bypass Read-Only Memory (ROM) that it has on SoC. If an issue occurs with the boot up code of CSE (TXE3.0) before the first patch point this strap enables the platform to tel CSE (TXE3.0) to bypass the ROM causing the issue and go to the patch space instead.
GPIO_40	RSVD	20K PD	Please ensure that this strap is pulled LOW when RSM_RST_N de-asserts for normal platform operation.
GPIO_43	Allow eMMC as a boot source	20K PU	1=enable (default) 0=disable
GPIO_44	Allow SPI as a boot source	20K PU	1=enable (default) 0=disable
GPIO_47	Force DNX FW Load	20K PD	1 = Force 0 = Do not force (default) Note: DNX: Download and Execute Note: This strap is a recovery strap for corrupted FW images. This strap will force CSE (TXE3.0) to execute a "Download and Execute" (DNX) flow, where it would fetch firmware from a USB stick and re-flash a USB CSE (TXE3.0) can do it for BIOS part of FW, but if CSE FW itself is corrupted we need this strap.
GPIO_48	RSVD	20K PD	Please ensure that this strap is pulled LOW when RSM_RST_N de-asserts for normal platform operation.
GPIO_78	SMBus 1.8V/3.3V mode select	20K PU	1=buffers set to 1.8V mode (default) 0=buffers set to 3.3V mode
GPIO_82	RSVD	20K PD	Please ensure that this strap is always pulled low for normal platform operation.
GPIO_88	PMU (Power Management Unit) 1.8V/3.3V mode select	20K PU	1=buffers set to 1.8V mode (default) 0=buffers set to 3.3V mode
GPIO_92	SMBus No Re-Boot	20K PD	1 = Enable 0 = Disable (default) Note: Platforms should strap this LOW. Functionality is handled by the FPC.
GPIO_104	RSVD	20K PD	Please ensure that this strap is pulled LOW when RSM_RST_N de-asserts for normal platform operation.
GPIO_105	RSVD	20K PD	Please ensure that this strap is pulled LOW when RSM_RST_N de-asserts for normal platform operation.
GPIO_106	RSVD	20K PU	Please ensure that this strap is pulled HIGH when RSM_RST_N de-asserts for normal platform operation.
GPIO_111	Boot BIOS Strap (BBS)	20K PU	1 = Do not boot from SPI (default) 0 = Boot from SPI
GPIO_118	Flash Descriptor Override	20K PD	0 = No Override (Normal Operation) 1 = Override Note: This strap enables the platform to override security features in the SPI.
GPIO_119	LPC 1.8V/3.3V mode select	20K PU	1=buffers set to 1.8V mode (default) 0=buffers set to 3.3V mode
GPIO_117	RSVD	20K PD	Please ensure that this strap is pulled LOW when RSM_RST_N de-asserts for normal platform operation.
GPIO_123	RSVD	20K PU	Please ensure that this strap is pulled HIGH when RSM_RST_N de-asserts for normal platform operation.
GPIO_112	RSVD	20K PD	Please ensure that this strap is pulled LOW when RSM_RST_N de-asserts for normal platform operation.
GPIO_113	RSVD	20K PD	Please ensure that this strap is pulled LOW when RSM_RST_N de-asserts for normal platform operation.
GPIO_120	Top swap override	20K PD	1 = Enable 0 = Disable (default) Note: Within the SPI ROM there may be different locations where the boot code is stored. This strap enables platform to change where the core will look for BIOS code for a SPI boot only.
GPIO_121	RSVD	20K PD	Please ensure that this strap is pulled LOW when RSM_RST_N de-asserts for normal platform operation.



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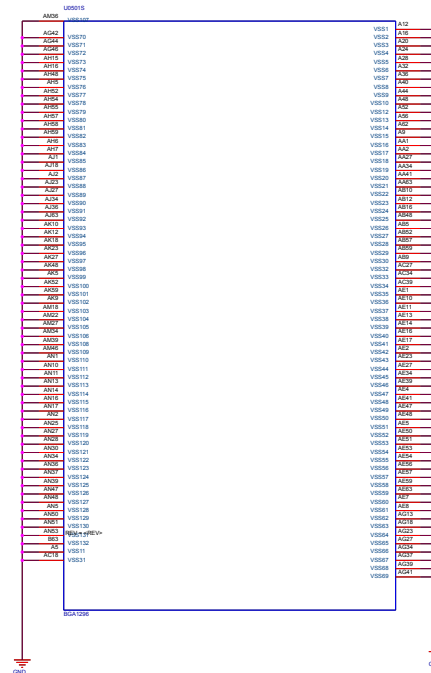
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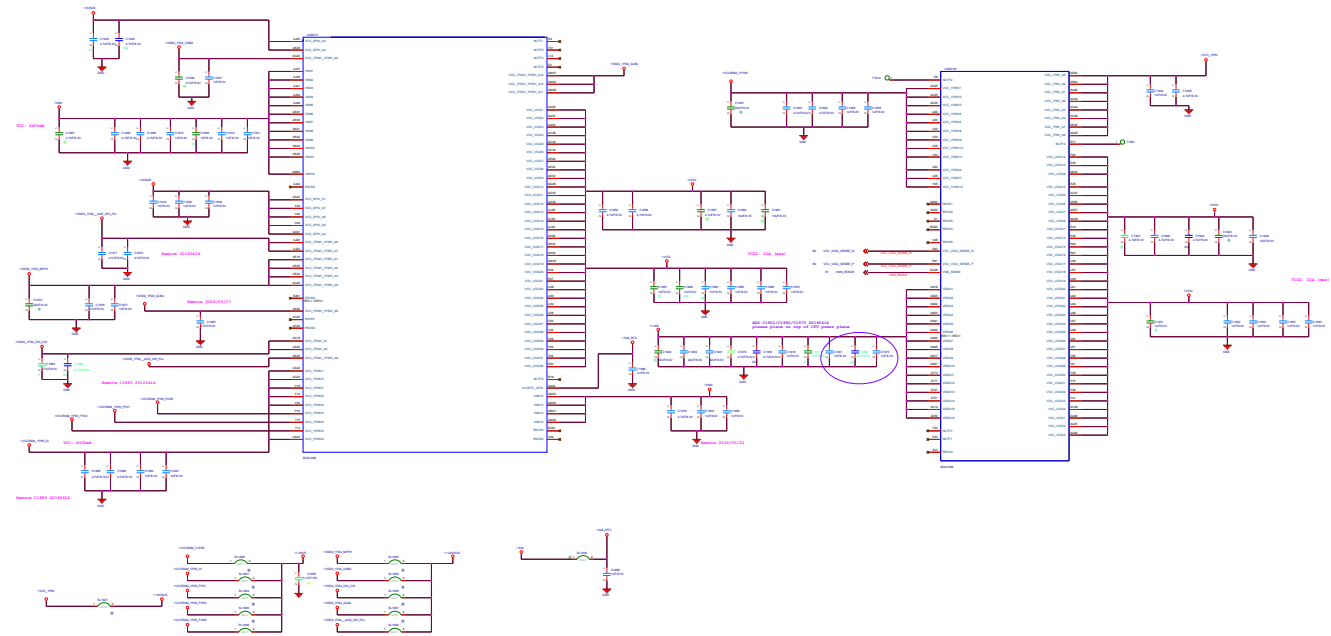
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- 16.CPU_RCOMP
- 17_CPU_STRAP
- 18.CPU_GND
- 19.CPU_POWER
- 20.Memory down
- 21.So-Dimm
- 22.***
23. CPU_PCH_CLK_GEN
- 24.*****
- 25.*****
- 26.*****
- 27.Debug
- 28.SPI ROM
- 29.****
- 30.EC_IT8995
- 31.KBC_KB & TP
- 32.RESET CIRCUIT
- 33.LAN_RTL8111GUX-CG
34. ***
- 35.****

Global Search

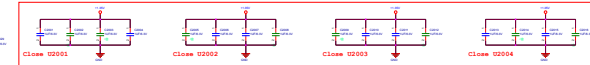
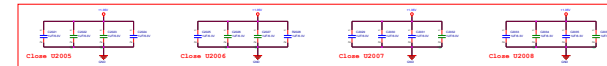
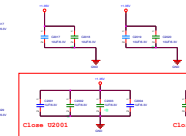
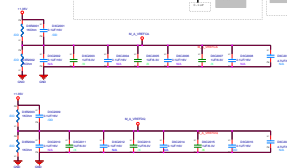
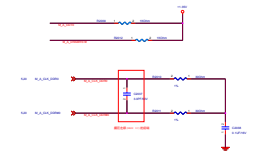
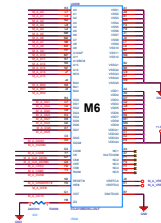
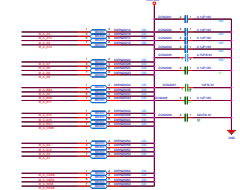
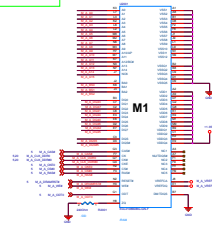
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- 11.CPU_SVID/SMBUS/USB2
- 12.CPU_SPI/I2C
- 13.CPU_PMU/UART
- 14.CPU_GPIO
- 15.CPU_RTC/PWM
- 16.CPU_RCOMP
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- 34.***
- 35.****

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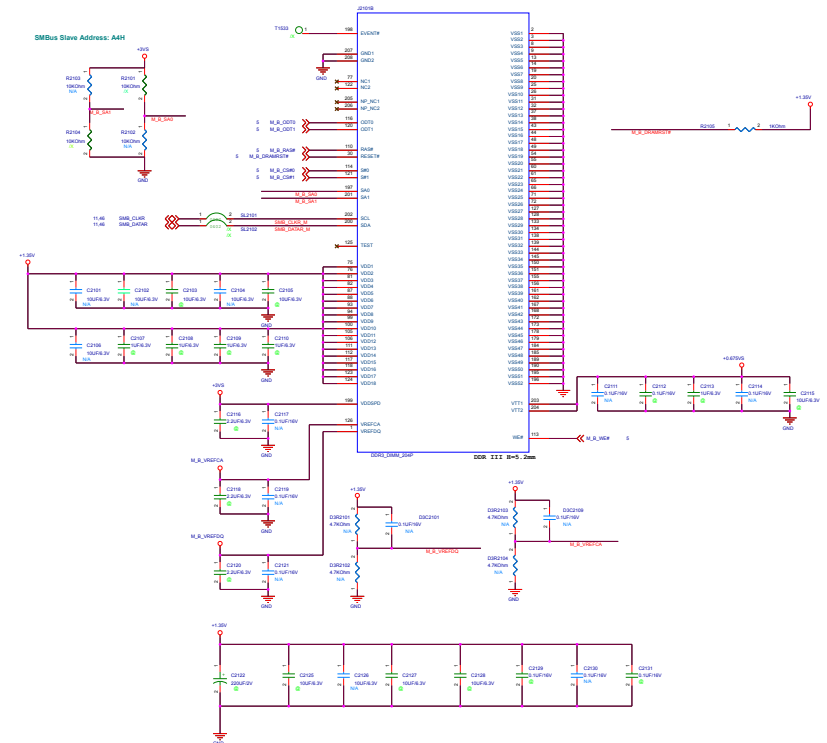
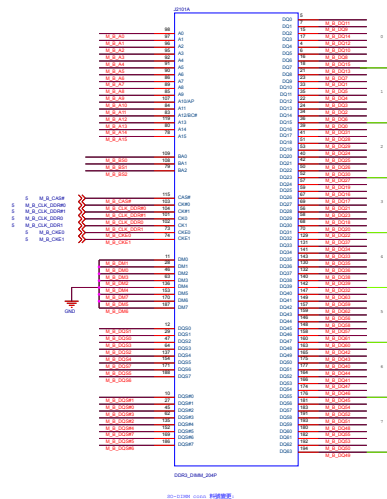
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SODIMM CHB-DIMM0

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- 11.CPU_SVID/SMBUS/USB2
- 12.CPU_SPI/I2C
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- 35.****

Global Search

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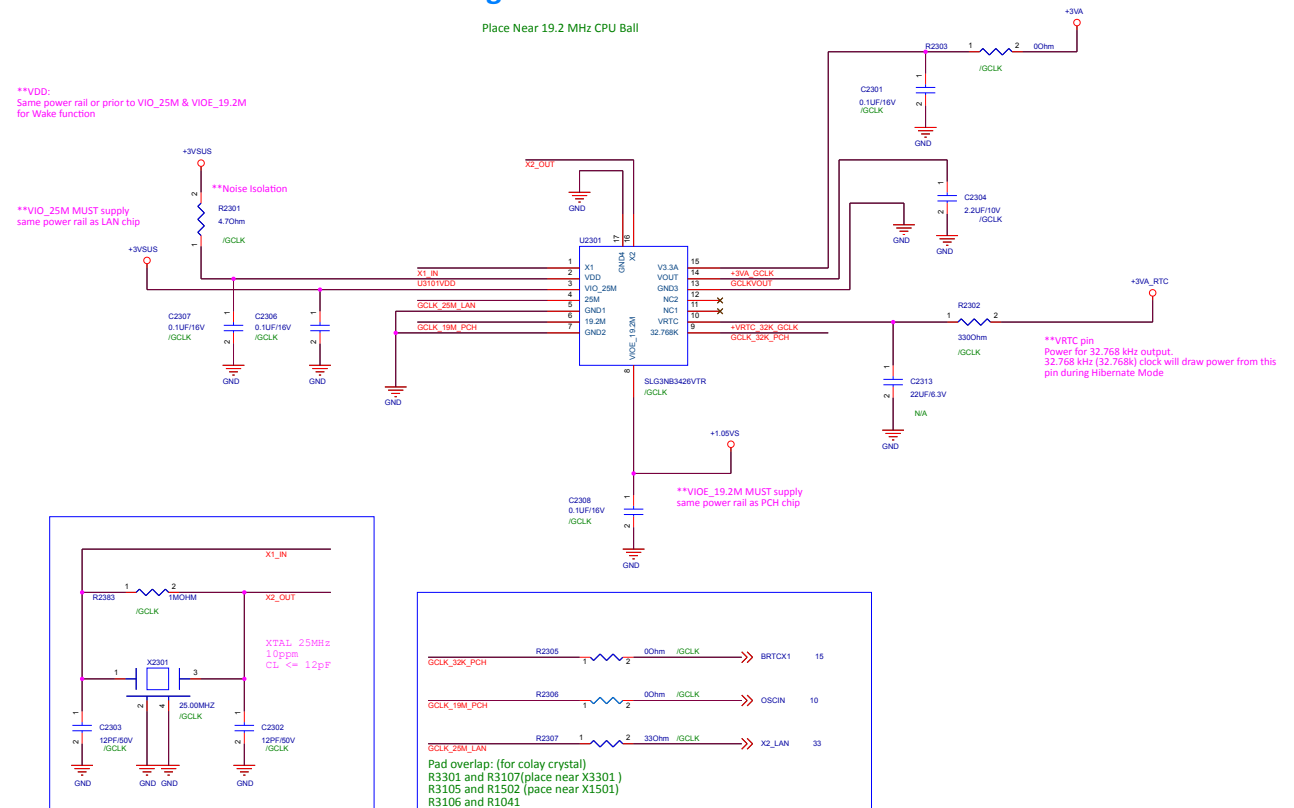
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Silego Green CLK

Place Near 19.2 MHz CPU Ball



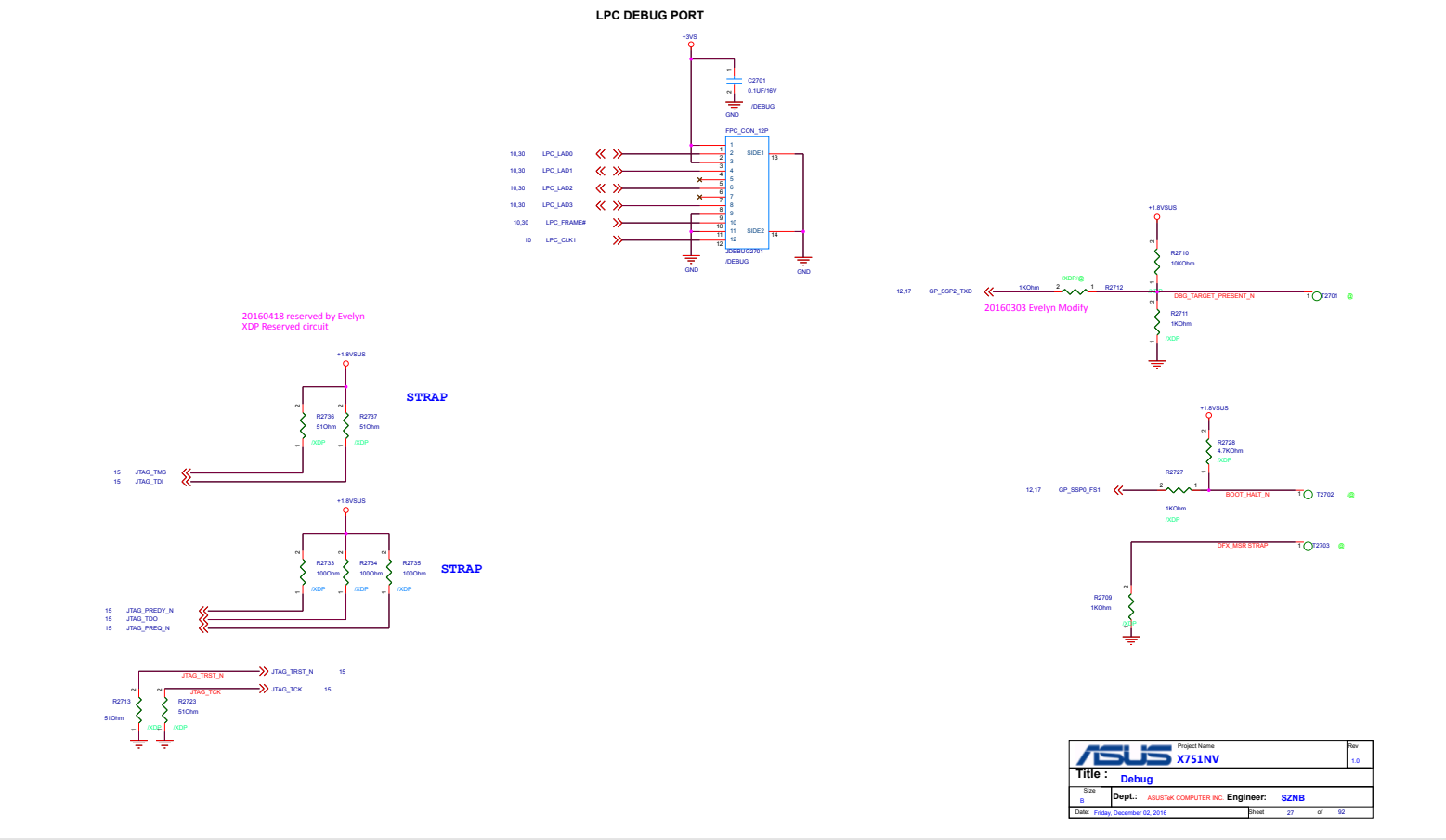
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- 11.CPU_SVID/SMBUS/USB2
- 12.CPU_SPI/I2C
- 13.CPU_PMU/UART
- 14.CPU_GPIO
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- 31.KBC_KB & TP
- 32.RESET CIRCUIT
- 33.LAN_RTL8111GUX-CG
- 34.***
- 35.****



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- 11.CPU_SVID/SMBUS/USB2
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- 35.****

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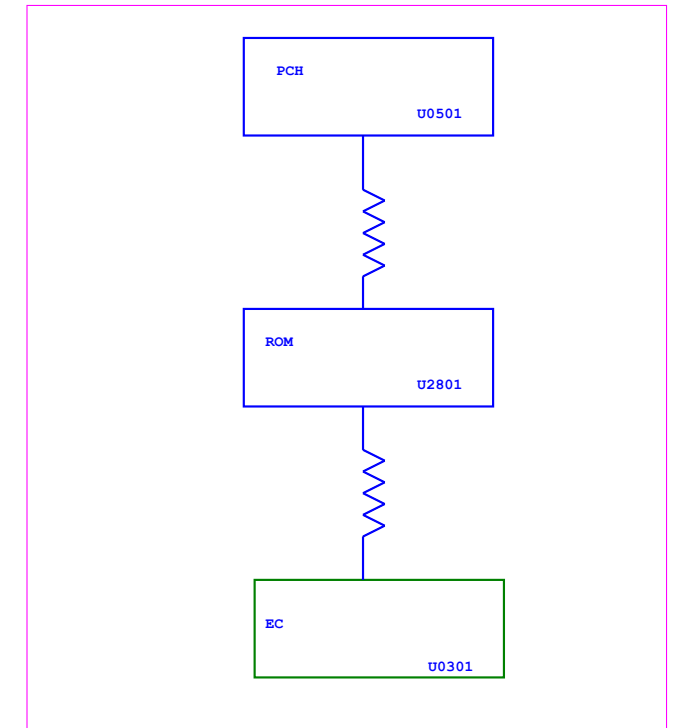
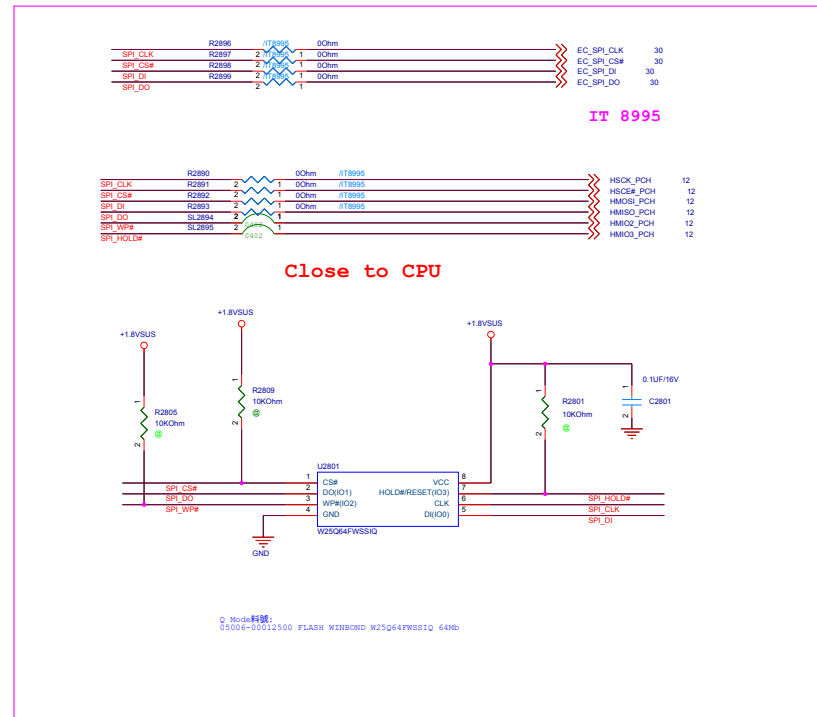
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- 33.LAN_RTL8111GUX-CG
- 34.***
- 35.****
- 36.AUD-ALC3236
- 37.Audio Jack
- 38.Speaker
- 39.***
- 40.LED_Screw
- 41.***
- 42_SD_Card
- 43.****
- 44.*****
- 45_eDP_CMOS_DMIC
- 46_DP2VGA_RTD2166
- 47_CRT_CONN
- 48_HDMI
- 49.***
- 50_Thermal Sensor & Fan
- 51.***
- 52.USB3.0/USB2.0_CON
- 53.WLAN
- 54.USB3.0 TYPEC CONNECTOR
- 55.***
- 56_POWER BD IO CONN
- 57.DSG_Discharge

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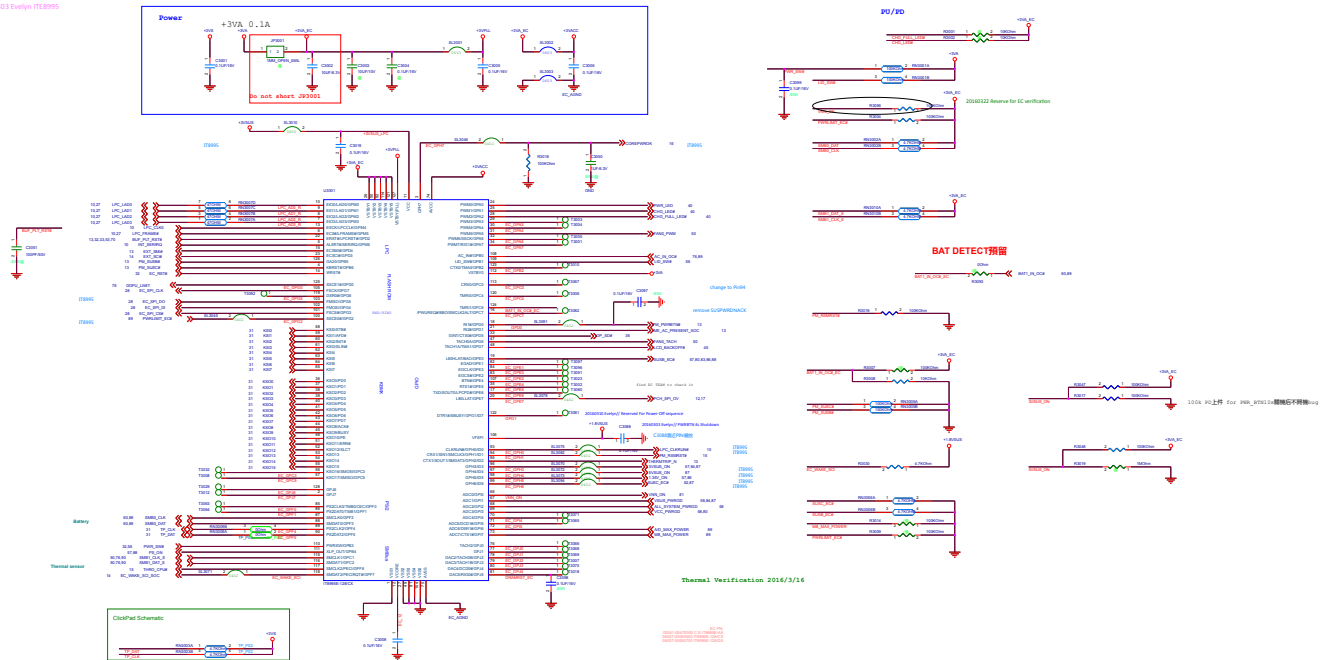
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20160303 Evelyn ITE8955



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- 36.AUD-ALC3236
- 37.Audio Jack
- 38.Speaker
- 39.***
- 40.LED_Screw
- 41.***
- 42_SD_Card
- 43.****
- 44.*****
- 45_eDP_CMOS_DMIC
- 46_DP2VGA_RTD2166
- 47_CRT_CONN
- 48_HDMI
- 49.***
- 50_Thermal Sensor & Fan
- 51.***
- 52.USB3.0/USB2.0_CON
- 53.WLAN
- 54.USB3.0 TYPEC CONNECTOR
- 55.***
- 56_POWER BD IO CONN
- 57.DSG_Discharge

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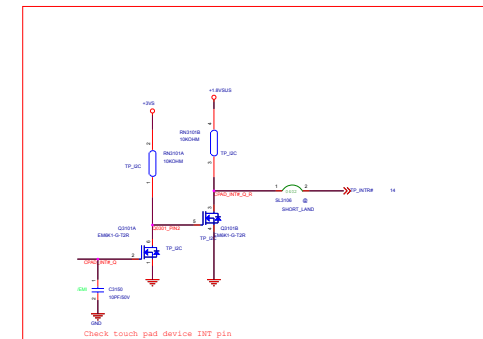
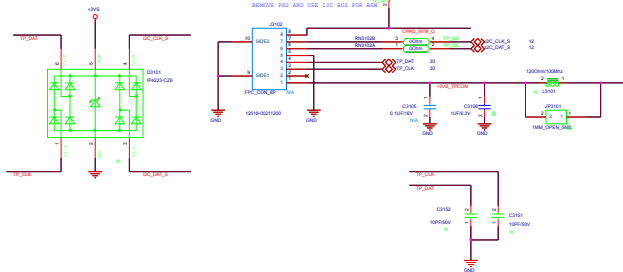
Keyboard Connector



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Touch Pad

TouchPad Connector



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33.LAN_RTL8111GUX-CG

34. ***

35.***

36.AUD-ALC3236

37.Audio Jack

38.Speaker

39.***

40.LED_Screw

41.*

42_SD_Card

43.***

44.*****

45_eDP_CMOS_DMIC

46_DP2VGA_RTD2166

47_CRT_CONN

48_HDMI

49.***

50_Thermal Sensor & Fan

51.***

52.USB3.0/USB2.0_CON

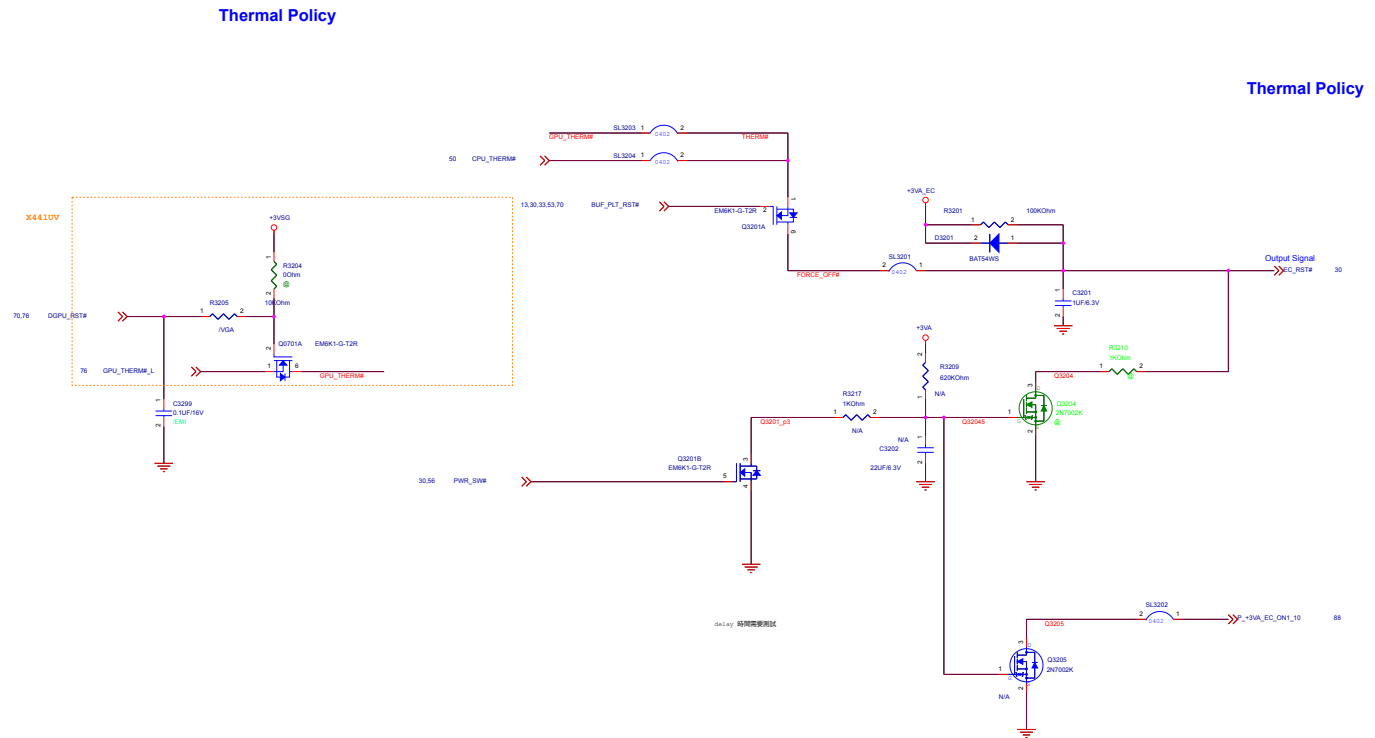
53. WLAN

54.USB3.0 TYPEC CONNECTOR

55.***

56_POWER BD IO CONN

57 DSG Discharge



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- 33.LAN_RTL8111GUX-CG
- 34.***
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- 37.Audio Jack
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- 40.LED_Screw
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- 44.*****
- 45_eDP_CMOS_DMIC
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- 47_CRT_CONN
- 48_HDMI
- 49.***
- 50_Thermal Sensor & Fan
- 51.***
- 52.USB3.0/USB2.0_CON
- 53.WLAN
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- 55.***
- 56_POWER BD IO CONN
- 57.DSG_Discharge

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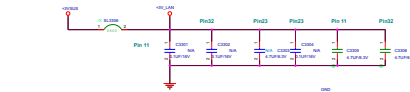
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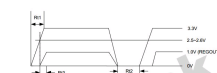
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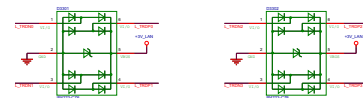
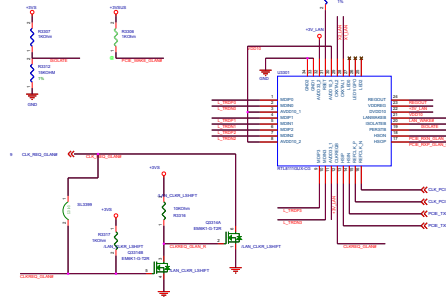
MoveTo



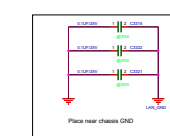
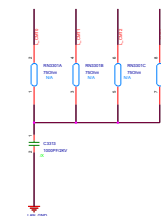
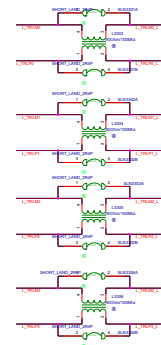
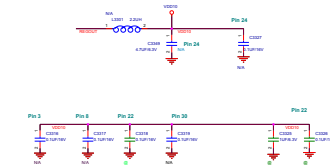
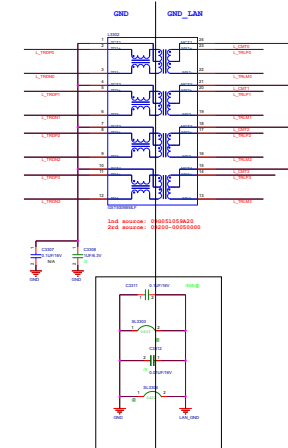
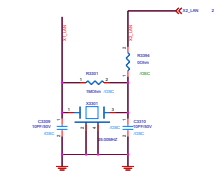
7. Power Sequence



Signal	Sequence	Time	Level	Notes
VDD	1.0V	t1	1.0V	
VDD	0.8V	t2	0.8V	
VDD	1.0V	t3	1.0V	



QW401 (24402)
R16 (746516)
QW402 (24402)
R17 (746516)
QW403 (24402)
R18 (746516)



ASUS	Title: LAN_RTL8111
Engineer: Compot_Lan	
YFS1NV	

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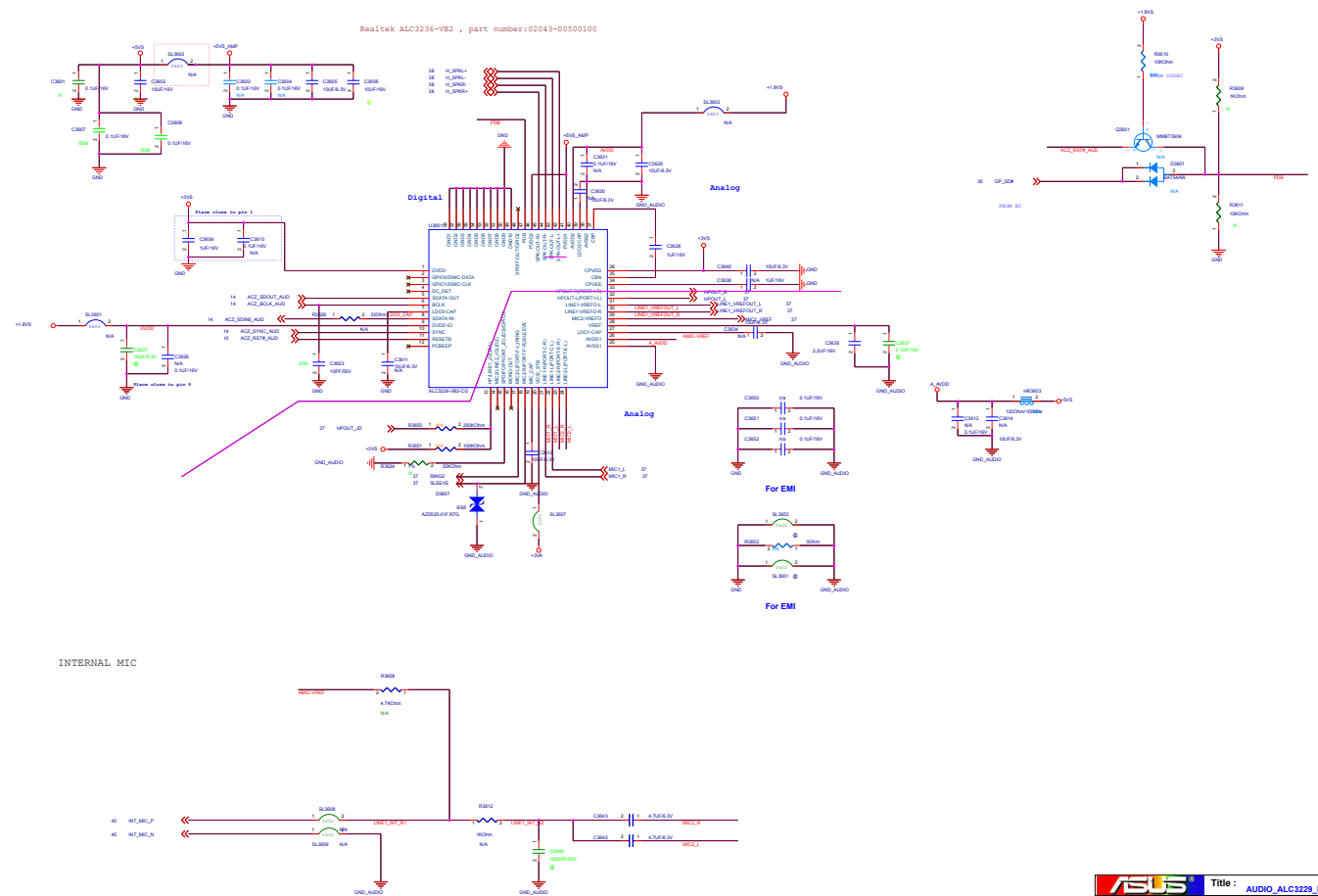
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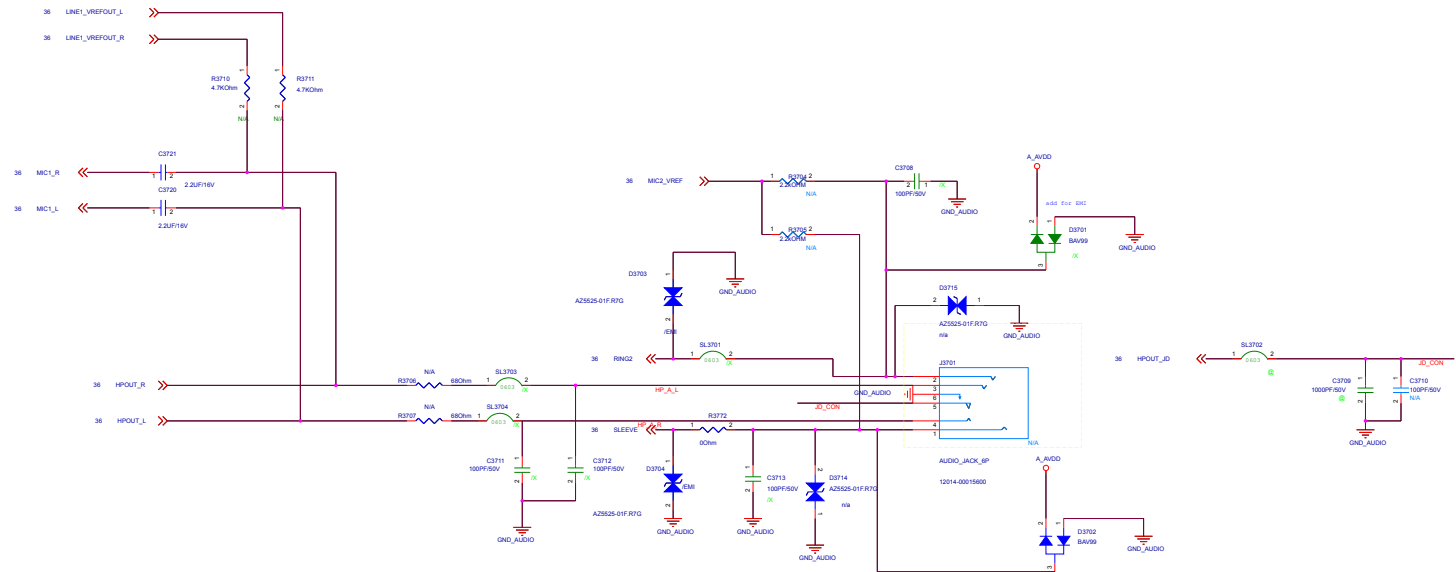
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33.LAN_RTL8111GUX-CG

34.***

35.****

36.AUD-ALC3236

37.Audio Jack

38.Speaker

39.***

40.LED_Screw

41.***

42_SD_Card

43.****

44.*****

45_eDP_CMOS_DMIC

46_DP2VGA_RTD2166

47_CRT_CONN

48_HDMI

49.***

50_Thermal Sensor & Fan

51.***

52.USB3.0/USB2.0_CON

53.WLAN

54.USB3.0 TYPEC CONNECTOR

55.***

56_POWER BD IO CONN

57.DSG_Discharge

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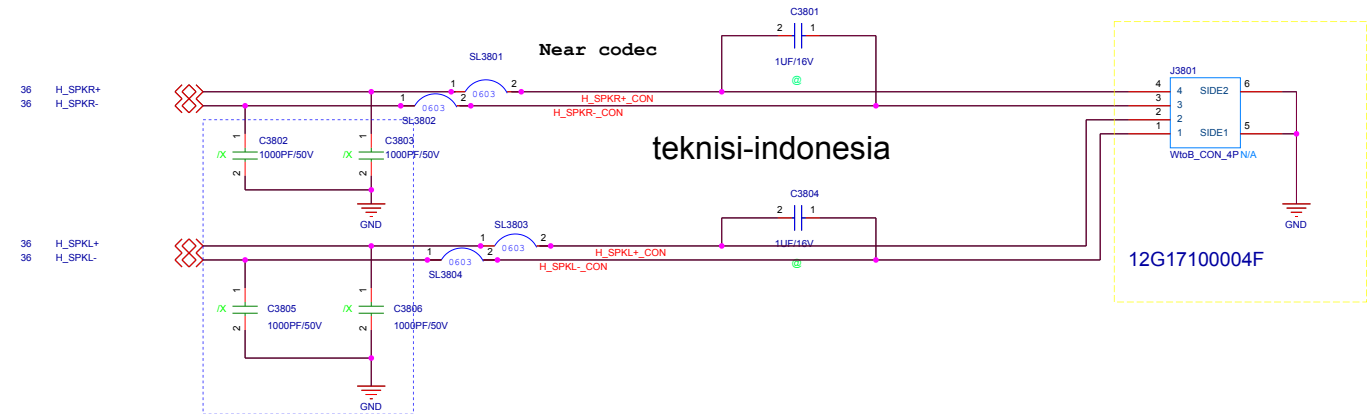
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33.LAN_RTL8111GUX-CG	
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36.AUD-ALC3236	
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38.Speaker	
39.***	
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43.****	
44.*****	
45_eDP_CMOS_DMIC	
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48_HDMI	
49.***	
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53.WLAN	
54.USB3.0 TYPEC CONNECTOR	
55.***	
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57.DSG_Discharge	

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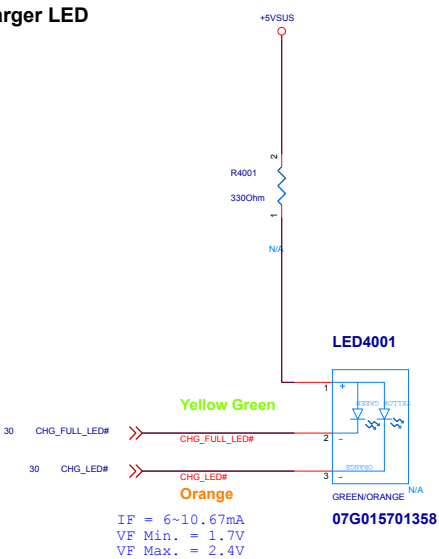
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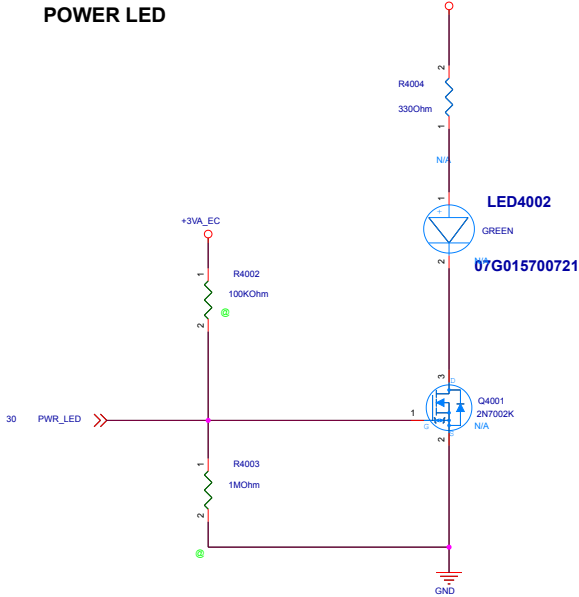
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Charger LED



POWER LED



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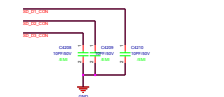
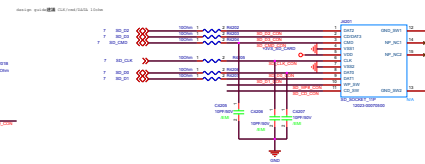
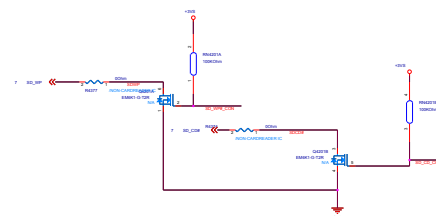
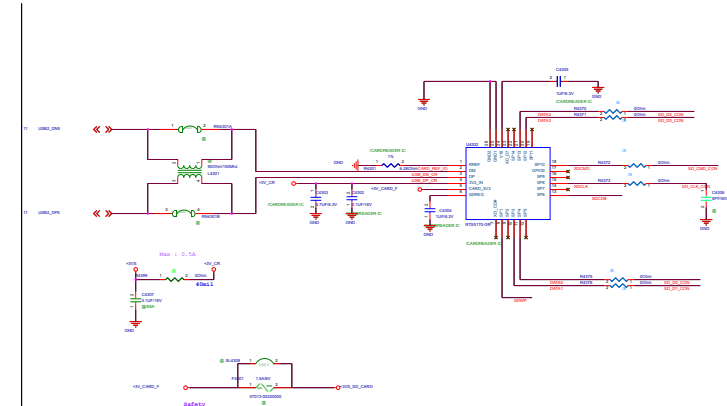
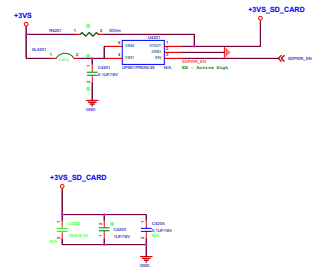
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54.USB3.0 TYPEC CONNECTOR

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56_POWER BD IO CONN

57.DSG_Discharge

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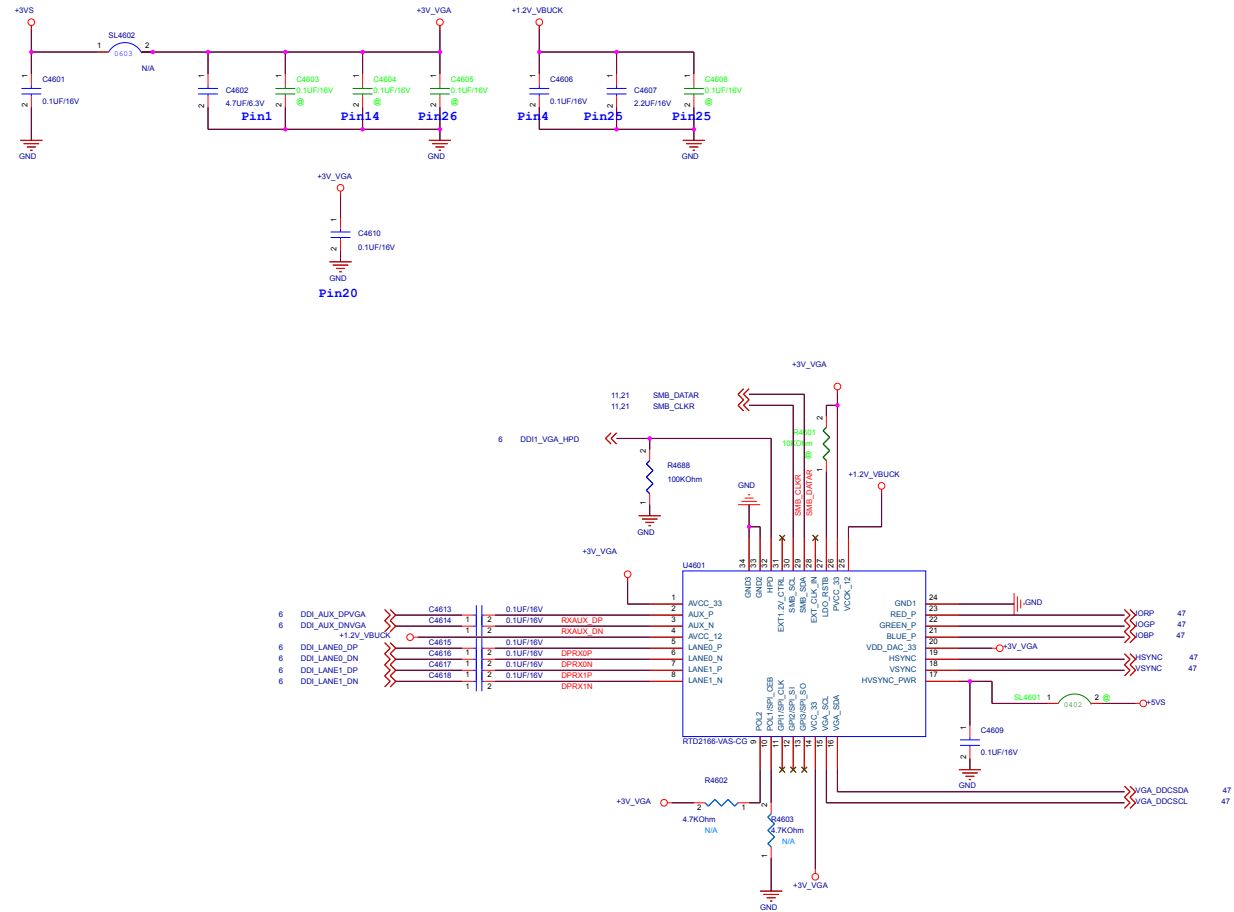
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- 40.LED_Screw
- 41.***
- 42_SD_Card
- 43.****
- 44.*****
- 45_eDP_CMOS_DMIC
- 46_DP2VGA_RTD2166
- 47_CRT_CONN
- 48_HDMI
- 49.***
- 50_Thermal Sensor & Fan
- 51.***
- 52.USB3.0/USB2.0_CON
- 53.WLAN
- 54.USB3.0 TYPEC CONNECTOR
- 55.***
- 56_POWER BD IO CONN
- 57.DSG_Discharge
- 58_PWR_Protect
- 59.POWER BD_IO_SWITCH_HAL
L SEN
- 60_DC_DC & BAT IN
- 61.HDD&ODD_CON
- 62.***
- 63.***

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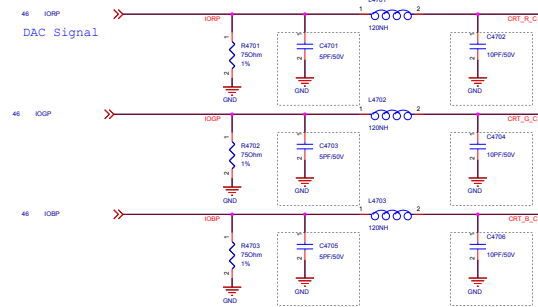
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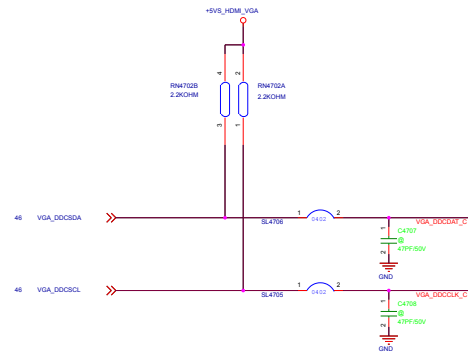
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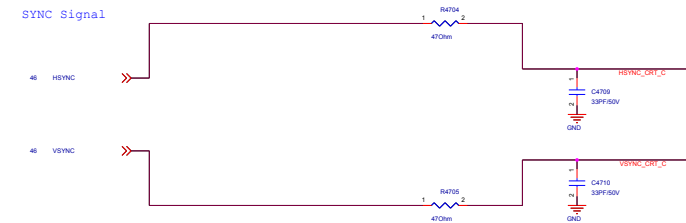
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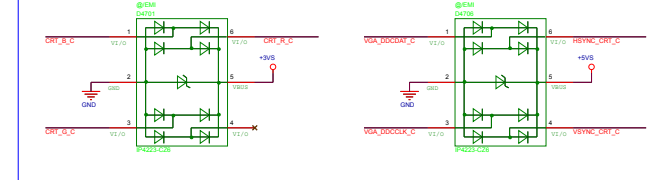
Control Signal



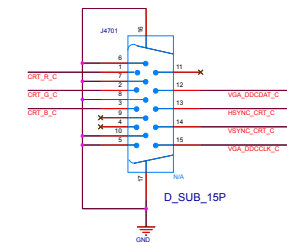
SYNC Signal



D-SUB ESD



CRT Connector



Project Name		Rev
ASUS X751NV		1.0
Title : CRT_DPtoCRT_Rev1.0-2		
Size	Dept.: ASUS&K COMPUTER INC.	Engineer: SZNB
Date: Friday, December 02, 2016	Sheet	47 of 62

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40.LED_Screw

41.***

42_SD_Card

43.***

44.*****

45_eDP_CMOS_DMIC

46_DP2VGA_RTD2166

47_CRT_CONN

48_HDMI

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53. WLAN

54.USB3.0 TYPEC CONNECTOR

55.***

56_POWER BD IO CONN

57.DSG_Discharge

58_PWR_Protect

59.POWER BD_IO_SWITCH_HAL
L SEN

60_DC_DC & BAT IN

61.HDD&ODD_CON

62.***

63.***

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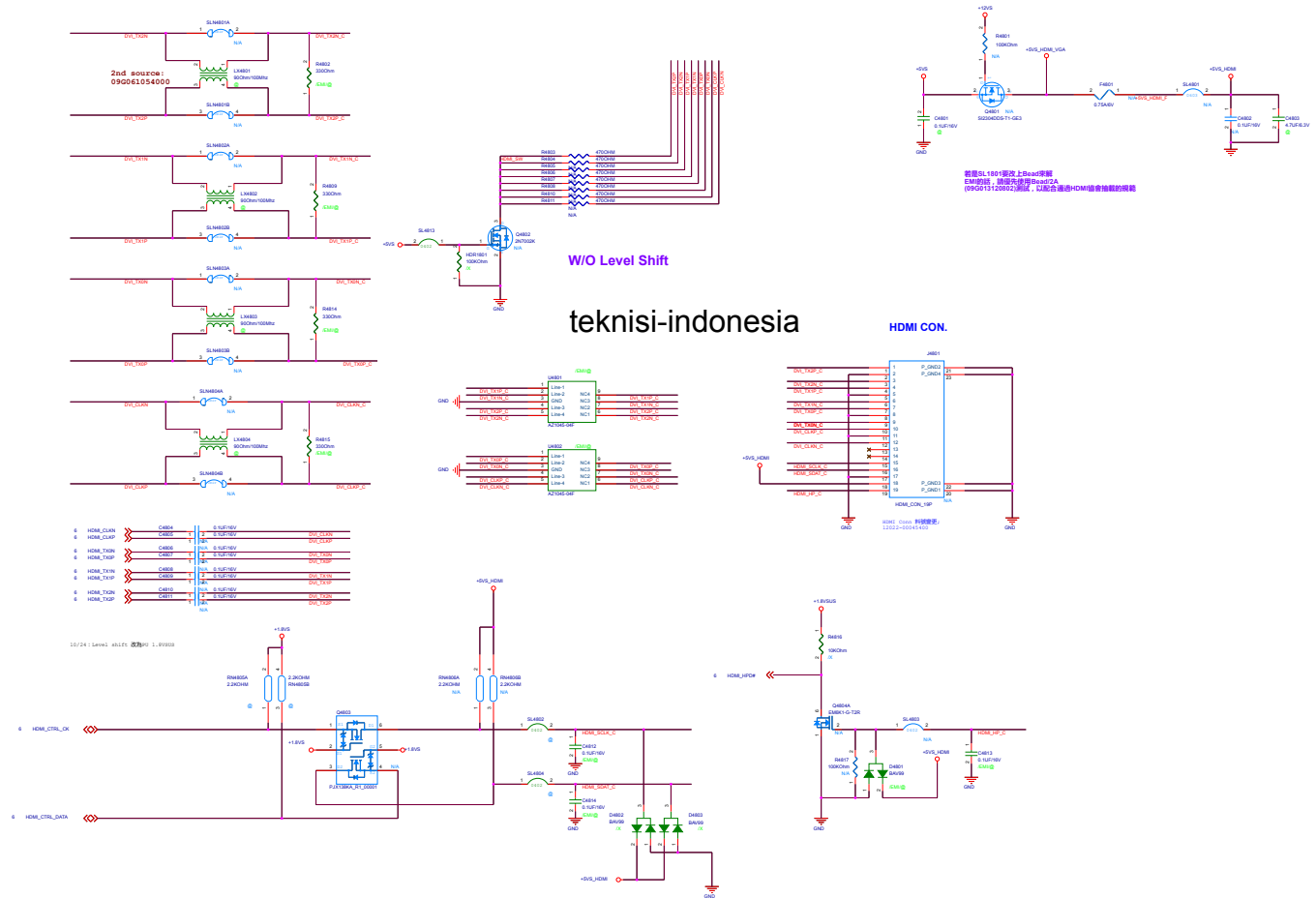
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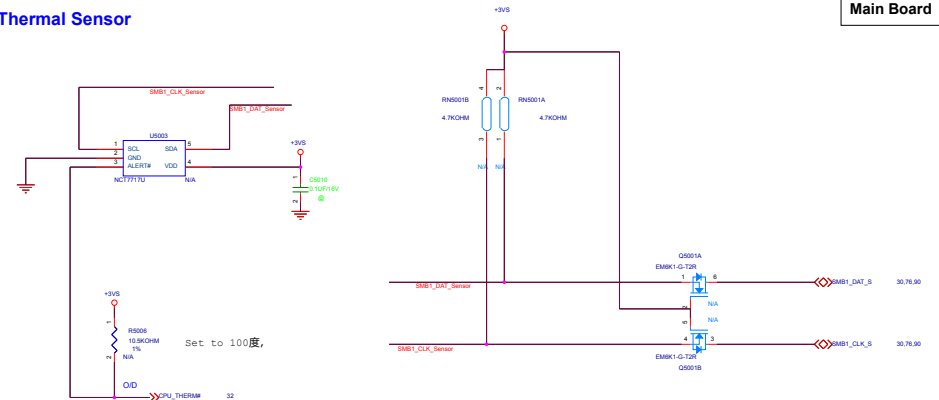
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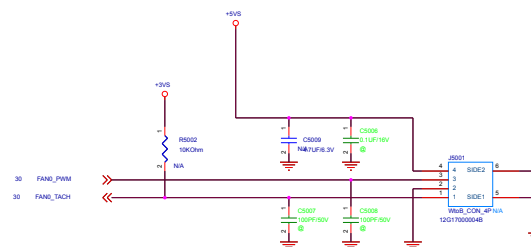
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DC FAN Control

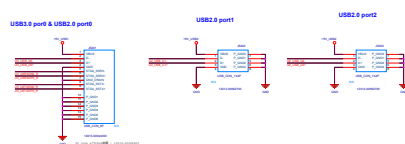
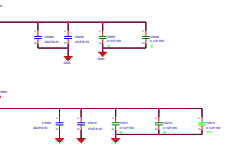
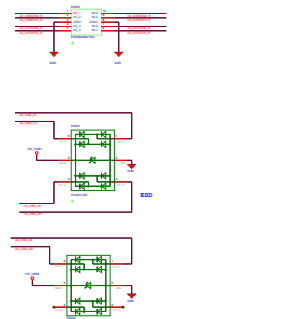
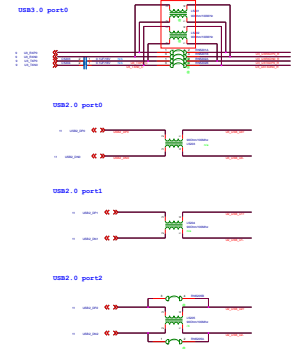
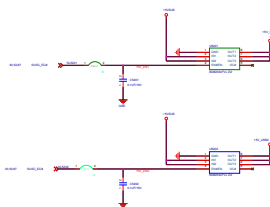


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60_DC_DC & BAT IN	
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54.USB3.0 TYPEC CONNECTOR

55.***

56_POWER BD IO CONN

57.DSG_Discharge

58_PWR_Protect

59.POWER BD_IO_SWITCH_HALL SEN

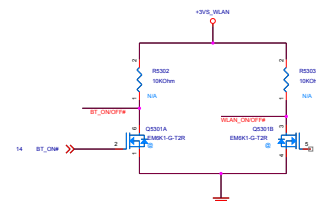
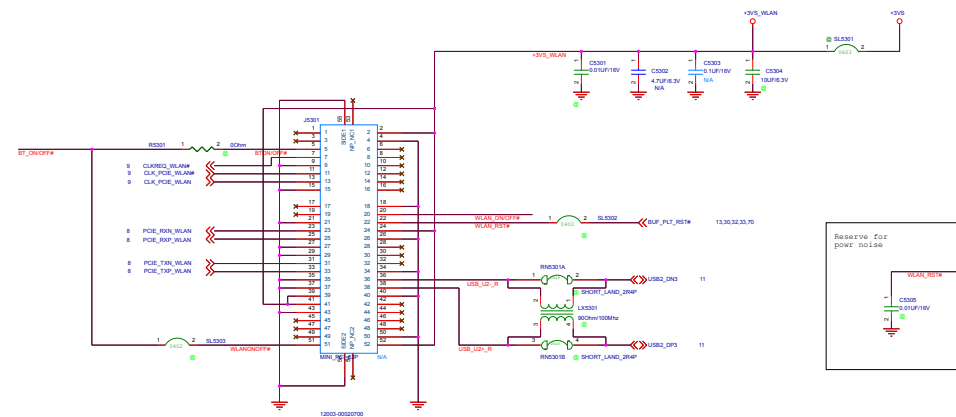
60_DC_DC & BAT IN

61.HDD&ODD_CON

62.***

63.***

WLAN con.



0521：注-對於0C011-00041300的輸入腳，BT&WLAN均為高電平ON

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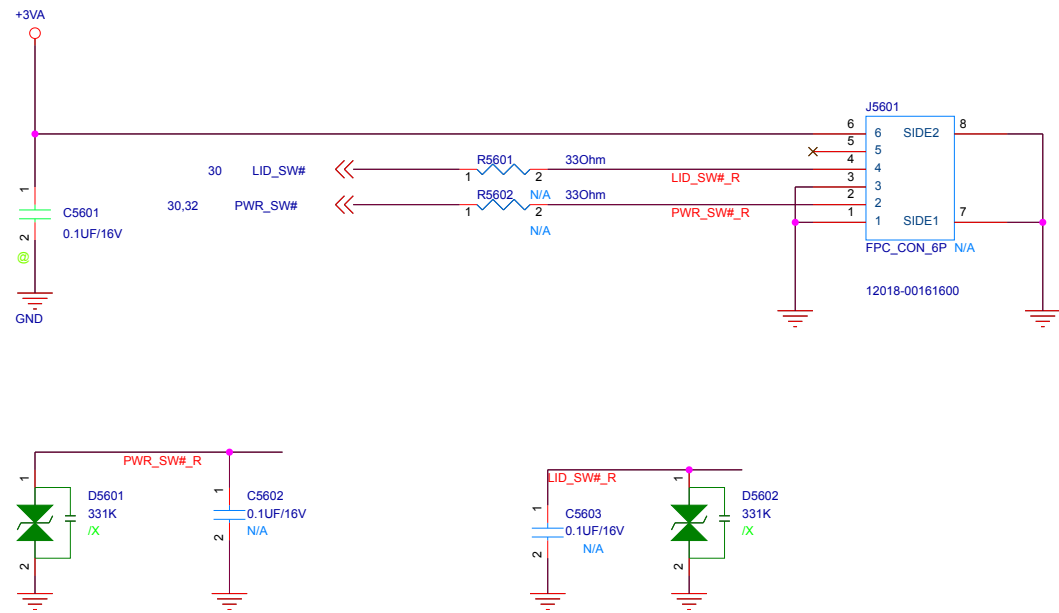
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POWER BD IO CONN



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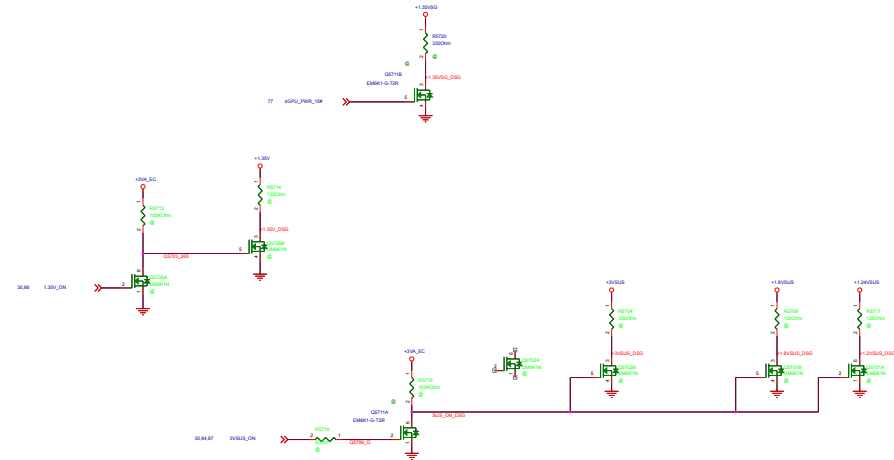
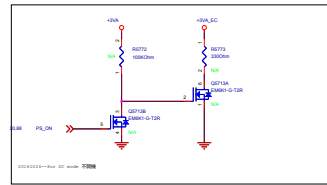
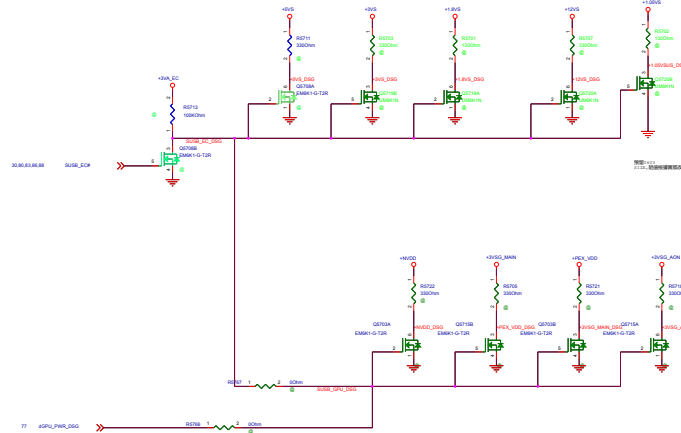
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Main Board



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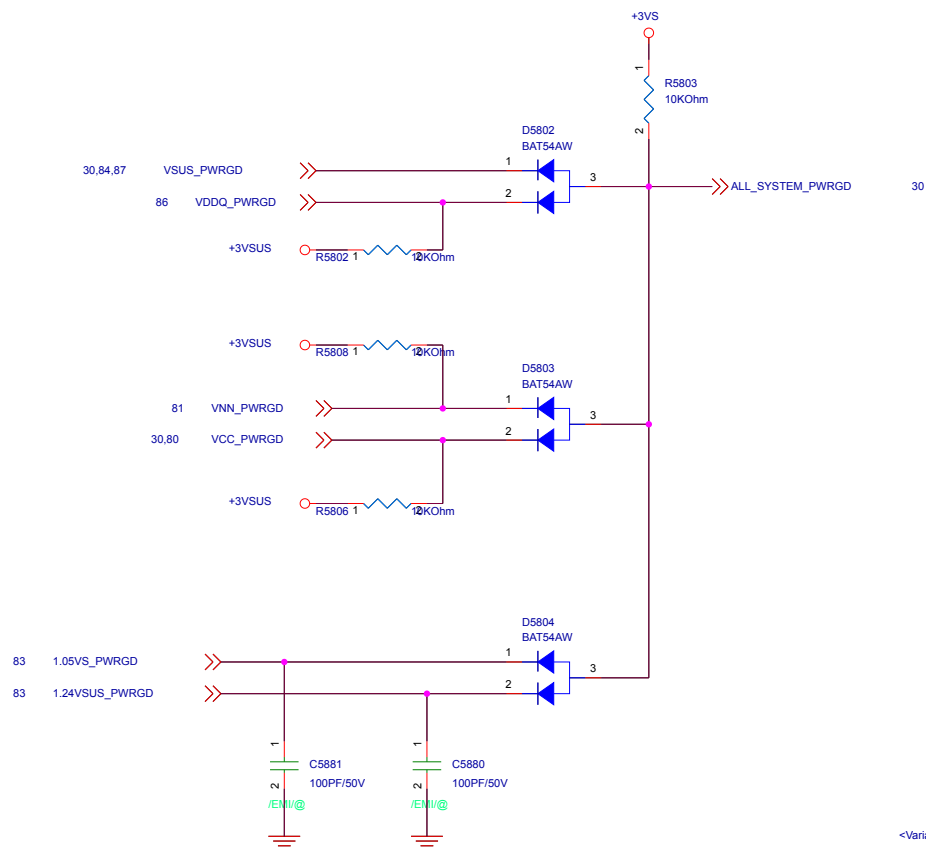
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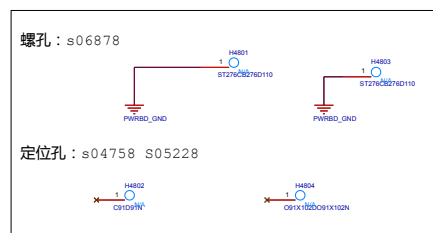
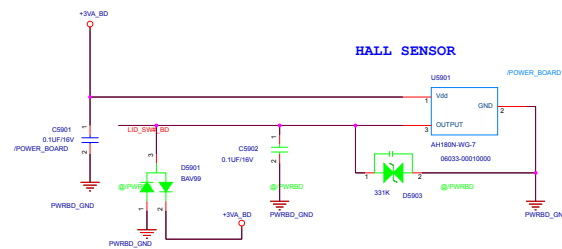
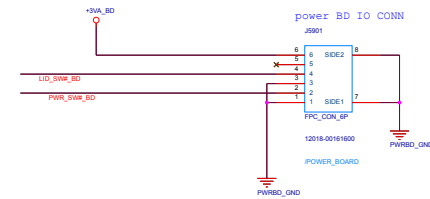
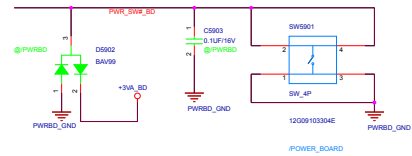
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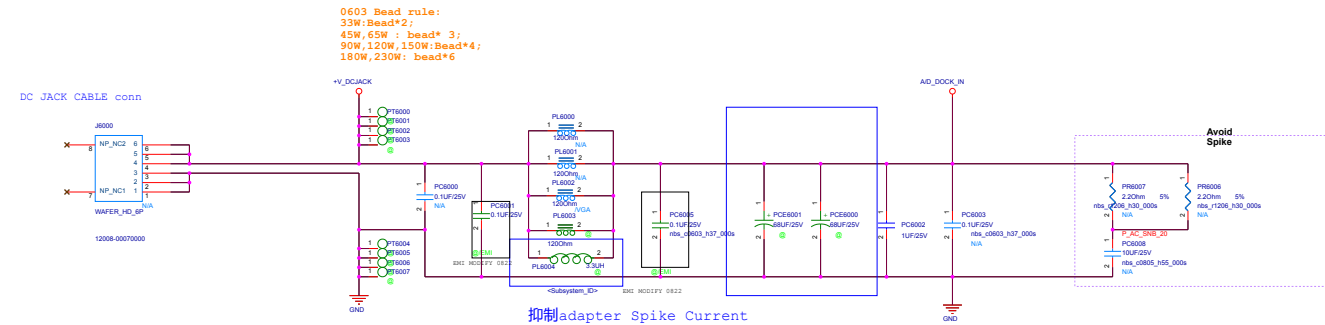
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- 72.VGA_nVIDIA_N16V/S_FB-DDR
- 73.VGA_nVIDIA_N16V/S_VDD
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- 79.VGA_****

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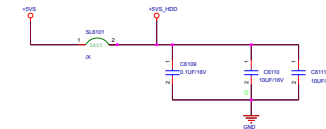
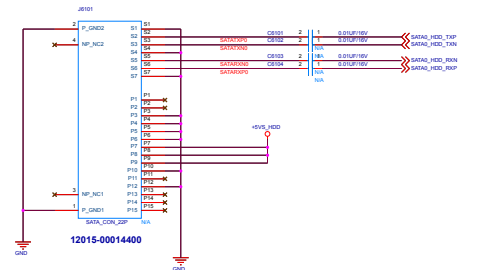
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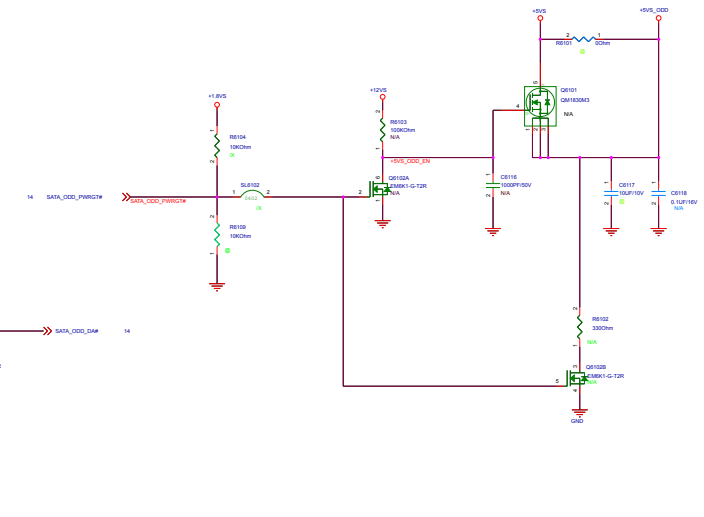
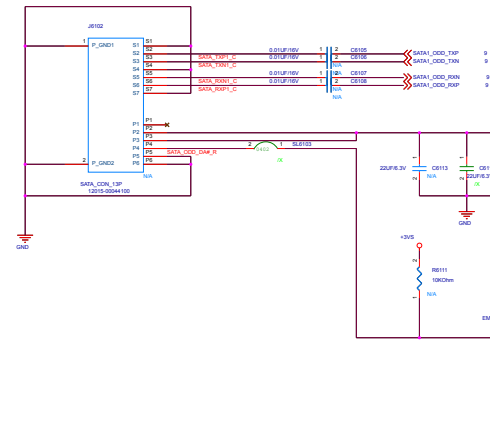
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SATA HDD Connector

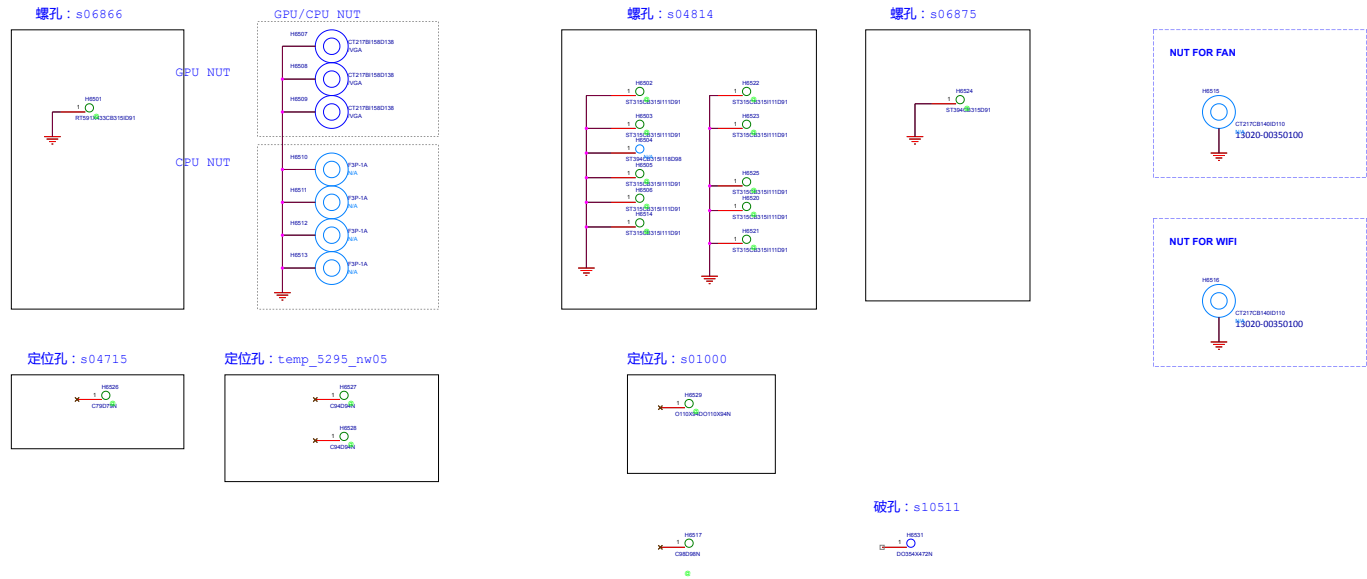


SATA ODD Connector



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72.VGA_nVIDIA_N16V/S_FB-DDR3	
73.VGA_nVIDIA_N16V/S_VDD	
74.VGA_nVIDIA_N16V/S_DISPLAY	
75.VGA_nVIDIA_N16V/S_ROM,XTAL	
76.VGA_nVIDIA_N16V/S_GPIO	
77.VGA_nVIDIA_N16V/S_POWER	
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69.EMI
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71.VGA_nVIDIA_N16V/S_FB-IF
72.VGA_nVIDIA_N16V/S_FB-DDR3
73.VGA_nVIDIA_N16V/S_VDD
74.VGA_nVIDIA_N16V/S_DISPLAY
75.VGA_nVIDIA_N16V/S_ROM,XTAL
76.VGA_nVIDIA_N16V/S_GPIO
77.VGA_nVIDIA_N16V/S_POWER
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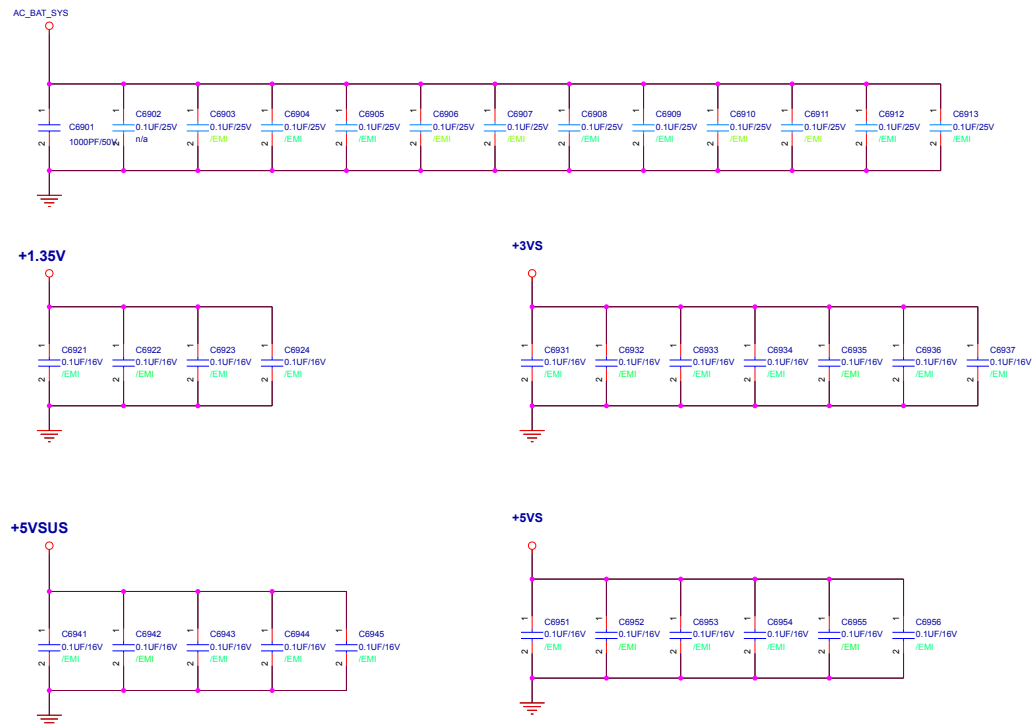
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76.VGA_nVIDIA_N16V/S_GPIO

77.VGA_nVIDIA_N16V/S_POWER

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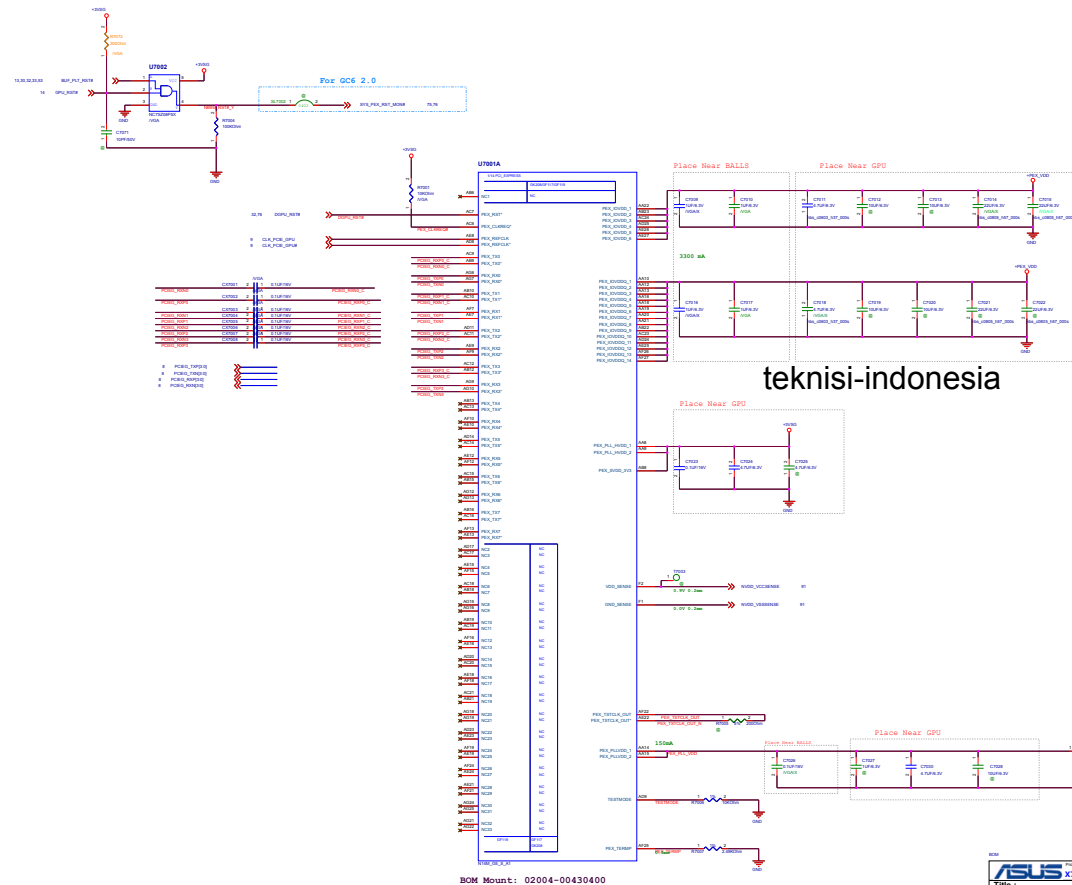
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58_PVVR_PIOlect

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65.SCREW HOLE, SMT NUT

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76.VGA_nVIDIA_N16V/S_GPIO

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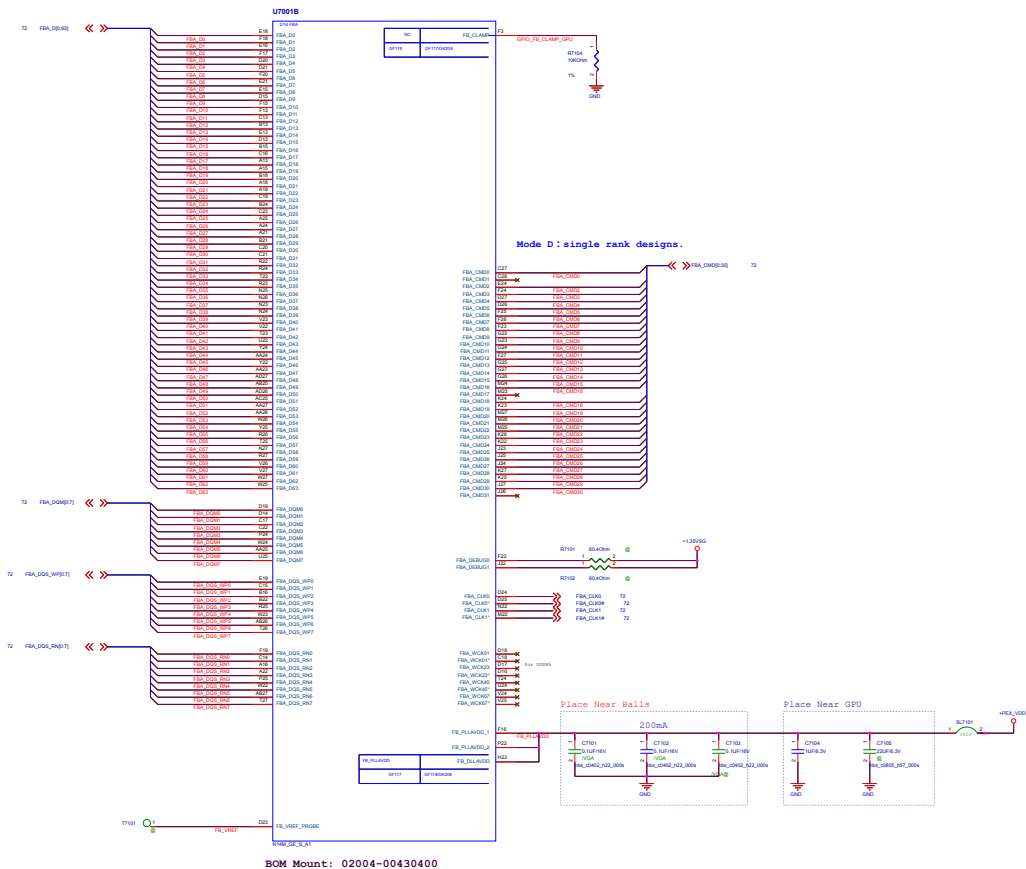
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77.VGA_nVIDIA_N16V/S_POWER
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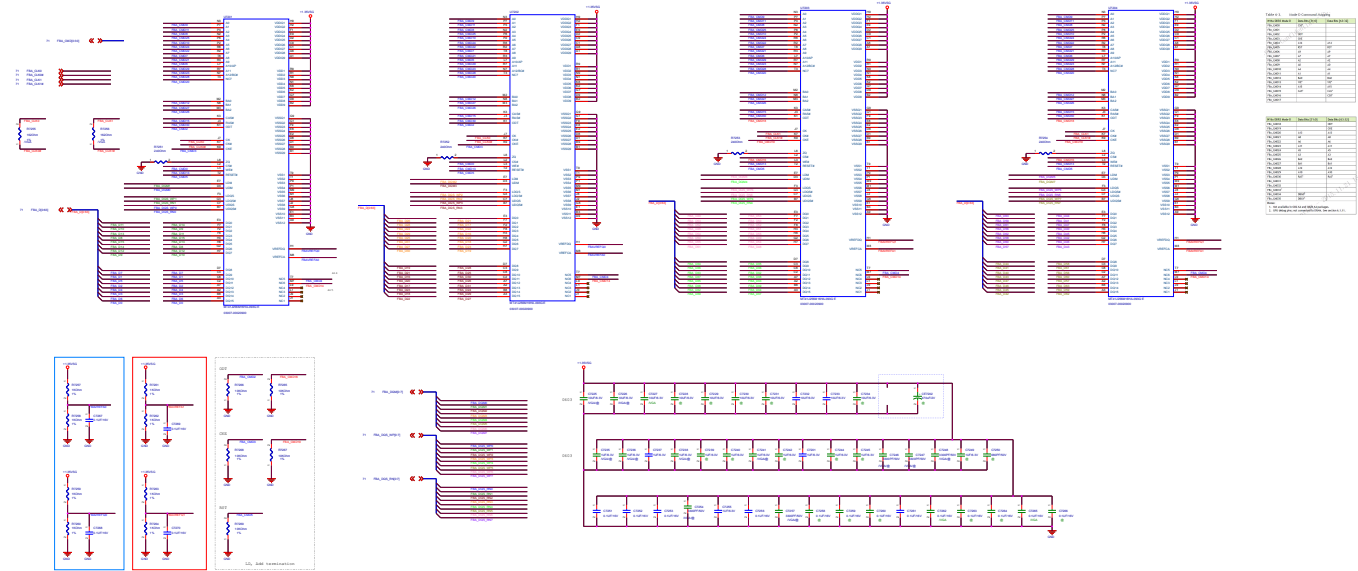
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73.VGA_nVIDIA_N16V/S_VDD

74.VGA_nVIDIA_N16V/S_DISPLA
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75.VGA_nVIDIA_N16V/S_ROM,XT
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76.VGA_nVIDIA_N16V/S_GPIO

77.VGA_nVIDIA_N16V/S_POWER

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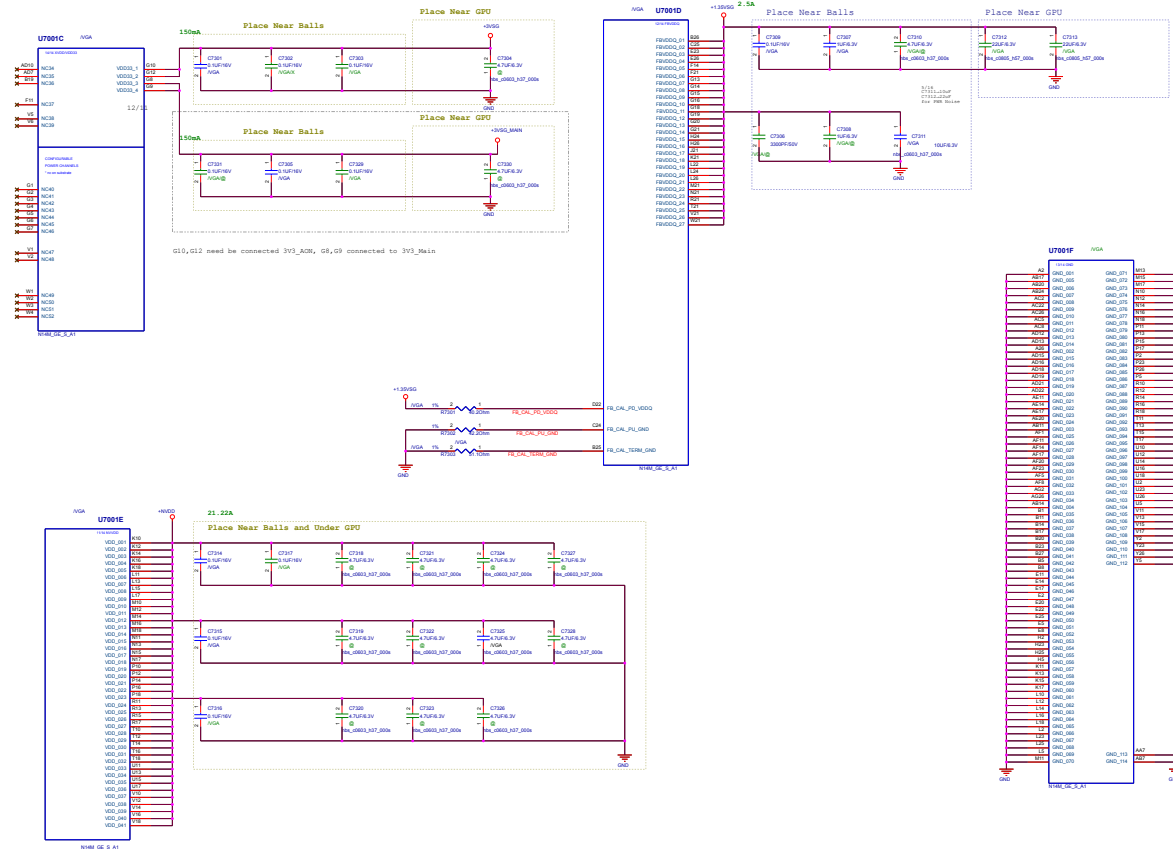
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58 PVVR Protect

59.POWER BD_IO_SWITCH_HALL SEN

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73.VGA nVIDIA N16V/S VDD

74.VGA_nVIDIA_N16V/S_DISPLA
Y

75.VGA_nVIDIA_N16V/S_ROM,XT
AL

76.VGA_nVIDIA_N16V/S_GPIO

77.VGA nVIDIA N16V/S POWER

78.VGA ****

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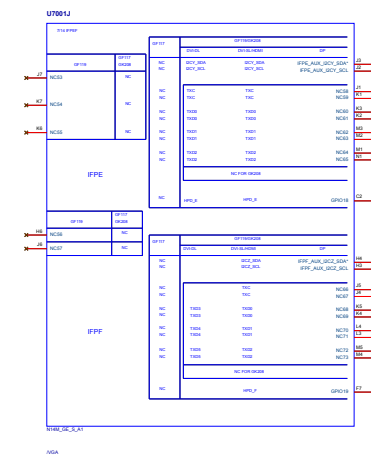
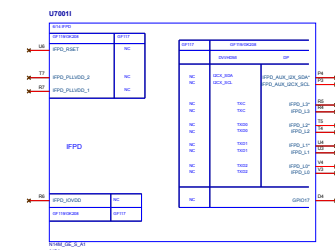
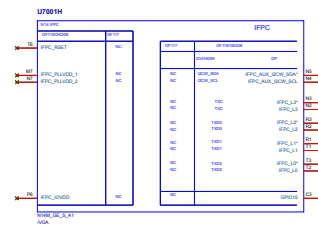
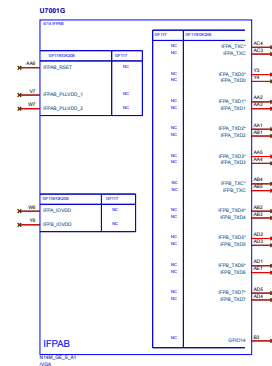
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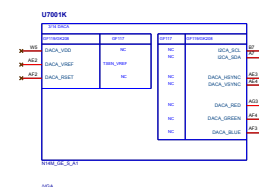
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59.POWER BD_IO_SWITCH_HALL SEN

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75.VGA_nVIDIA_N16V/S_ROM,XT
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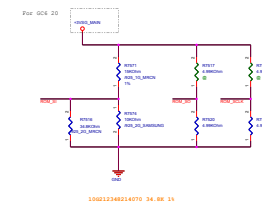
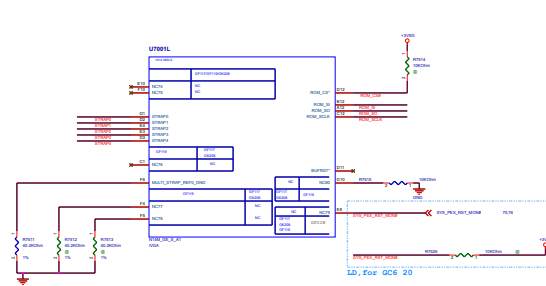
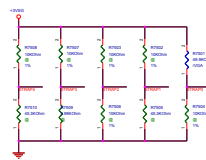
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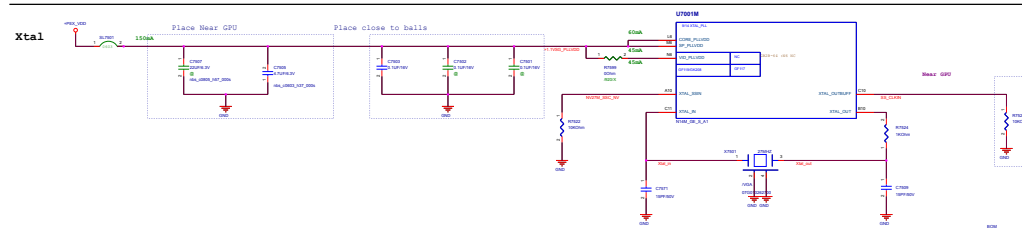
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VRAM (4顆)	2Gb(128*16)	Micron (03007-00030400)	GD0R3 128M*16 1.35V FBGA-96	MICRONMT41J128M16GT-093QX	0XA 15K FU
		三星 (03007-00021200)	GD0R3 256M*16 1.35V FBGA-96	SAMSUNGK4W4G1646E-BC1A	0XI 10K PD
	4Gb(256*16)				
		Micron (03007-00021300)	GD0R3 256M*16 1.35V FBGA-96	MICRONMT4J256M16LY-091GN	0X8 34.8K PD



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58.PWR_Protect

59.POWER BD_IO_SWITCH_HALL SEN

60.DC_DC & BAT IN

61.HDD&ODD_CON

62.***

63.***

64.***

65.SCREW HOLE, SMT NUT

66.***

67.***

68.***

69.EMI

70.VGA_nVIDIA_N16V/S_PCIE

71.VGA_nVIDIA_N16V/S_FB-IF

72.VGA_nVIDIA_N16V/S_FB-DDR 3

73.VGA_nVIDIA_N16V/S_VDD

74.VGA_nVIDIA_N16V/S_DISPLAY

75.VGA_nVIDIA_N16V/S_ROM,XTAL

76.VGA_nVIDIA_N16V/S_GPIO

77.VGA_nVIDIA_N16V/S_POWER

78.VGA_****

79.VGA_****

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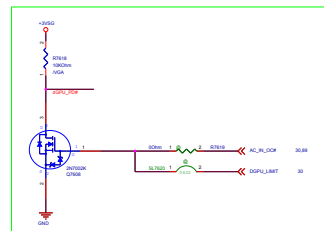
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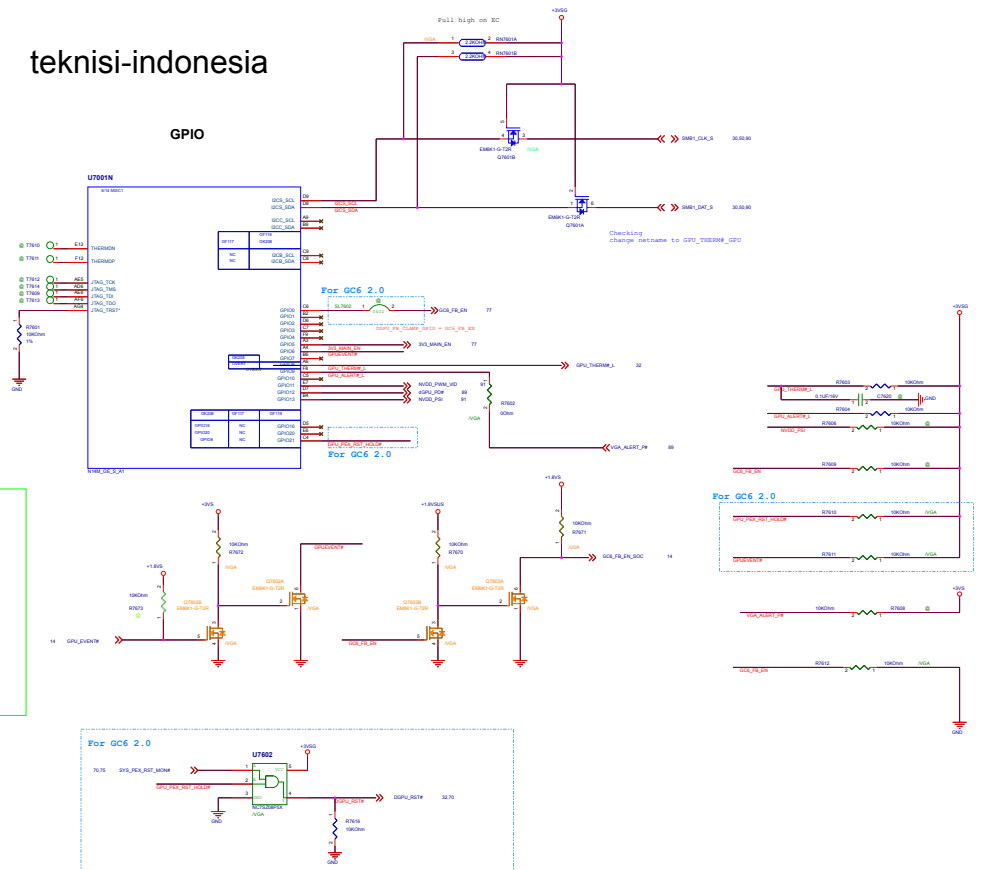
Table 12-1. G628-64 and G648-128 GPIO Description

Pin Name	Normal Function	I/O	Functional Description	Recommended Default Pull-up or Pull-down
GP000	FB_CLAMP_AON	I	FB Clamp monitor for GCs 1.0	10K pull-down to GND
GP001	GCs_FB_EN	O	FB Enable for GCs 2.0	10K pull-down to GND
GP002	MEM_VDD_CTL	O	Memory VDD VDD	10K pull-down to GND
GP003	LCD_BL_PWM	O	Panel Backlight PWM Brightness Control	10K pull-down to GND
GP004	LCD_VCC	O	Panel Power Enable	10K pull-down to GND
GP005	LCD_BL_EN	O	Panel Backlight Enable	10K pull-down to GND
GP006	3V3_JAHL_EN	O	GPU proper up/down	10K pull-up to 3V3_AON
GP007	FB_CLAMP_TGL_REQ	O	Clamp/Unclamp request for GCs 1.0	10K pull-up to system 3.3V
GP008	GPU_EVENTF	I	GPU wake signal for GCs 2.0	10K pull-up to 3V3_AON
GP009	3D Vision	O	3D Vision LID signal	10K pull-down to GND
GP010	SYS_PEX_RST_NQRM	I	System side PCIe reset Monitor	10K pull-down to GND
GP011	ALERT	I/O	Active Low Thermal Alert	10K pull-up to 3V3_AON
GP012	MEM_VDD_CTL	O	Memory VDD Control	10K pull-down to GND
GP013	PWM_VDD	O	GPU Core VDD PWM control signal	10K pull-down to GND
GP014	PWR_LEVEL	I	AC power detect or power supply over/under input	10K pull-up to 3V3_AON
GP015	PSI	O	Phase Shedding	10K pull-up to 3V3_AON to enable two phase.
GP016	HFD_A	I	Hot Plug Detect for HFD used as DisplayPort or for HFD when used as Dual Link DVI	See Figure 12-1
GP017	HFD_C	I	Hot Plug Detect for HFC	See Figure 12-1
GP018	RESERVED			
GP019	HFD_D	I	Hot Plug Detect for HFD	See Figure 12-1
GP020	HFD_E	I	Hot Plug Detect for HFE	See Figure 12-1
GP021	HFD_F or HFD_B	I	Hot Plug Detect for HFF or HFB when used as DisplayPort	See Figure 12-1
GP022	Reserved			
GP023	GPU_PEX_RST_NQRM	O	GPU PCIe self-reset control	10K pull-up to 3V3_AON
OVERT	OVERT	O	Active Low Thermal Catastrophic Over Temperature	10K pull-up to 3V3_AON



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GPIO



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58.PWR_Protect

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60.DC_DC & BAT IN

61.HDD&ODD_CON

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63.***

64.***

65.SCREW HOLE, SMT NUT

66.***

67.***

68.***

69.EMI

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72.VGA_nVIDIA_N16V/S_FB-DDR 3

73.VGA_nVIDIA_N16V/S_VDD

74.VGA_nVIDIA_N16V/S_DISPLAY

75.VGA_nVIDIA_N16V/S_ROM,XTAL

76.VGA_nVIDIA_N16V/S_GPIO

77.VGA_nVIDIA_N16V/S_POWER

78.VGA_****

79.VGA_****

Global Search

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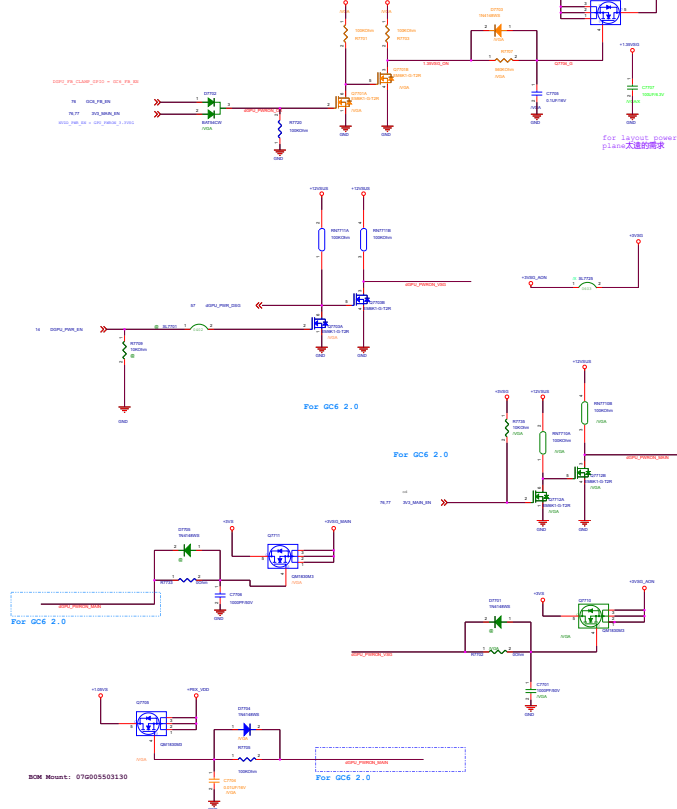
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dGPU IO Power Sequence

For GC6 2.0



dGPU Core Power Sequence

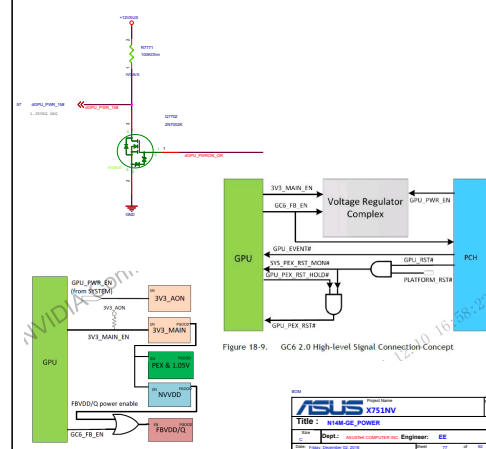
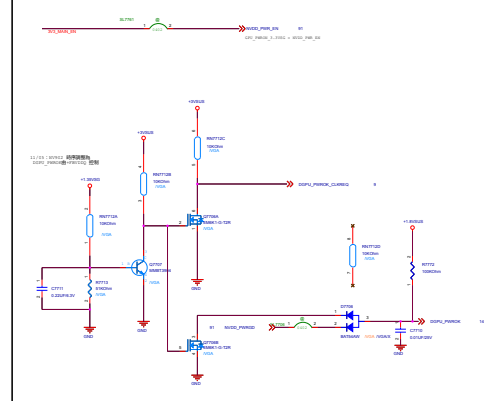


Figure 18-9. GC6 2.0 High-level Signal Connection Concept

ASUS	
Title: N16M-GE_POWER	
Doc No: N16M-GE_POWER	Rev: 1.0
Author: ASUS	Engineer: EE
Date: 2016-09-20	

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73.VGA_nVIDIA_N16V/S_VDD

74.VGA_nVIDIA_N16V/S_DISPLA
Y75.VGA_nVIDIA_N16V/S_ROM,XT
AL

76.VGA_nVIDIA_N16V/S_GPIO

77.VGA_nVIDIA_N16V/S_POWER

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79.VGA_****

80_PW_APL_VCGI(ATK1602GQ
W)

81_PW_APL_VNN(ATK1602GQW)

82_PW_***

83_PW_+1.24VSUS / + 1.05VSUS

84_PW_+1.8VSUS(UP9021)

85_PW_***

86_PW_+1.35V/+0.675VS(UP900
2)87_PW_+3VSUS/+5VSUS (UP900
3)

88_PW_LOAD SWITCH

89.PW_CHARGER(SN2867RUYR)

90.PW_PROTECTION

91.PW_DGPU_2PHASE(uP9013)

Global Search

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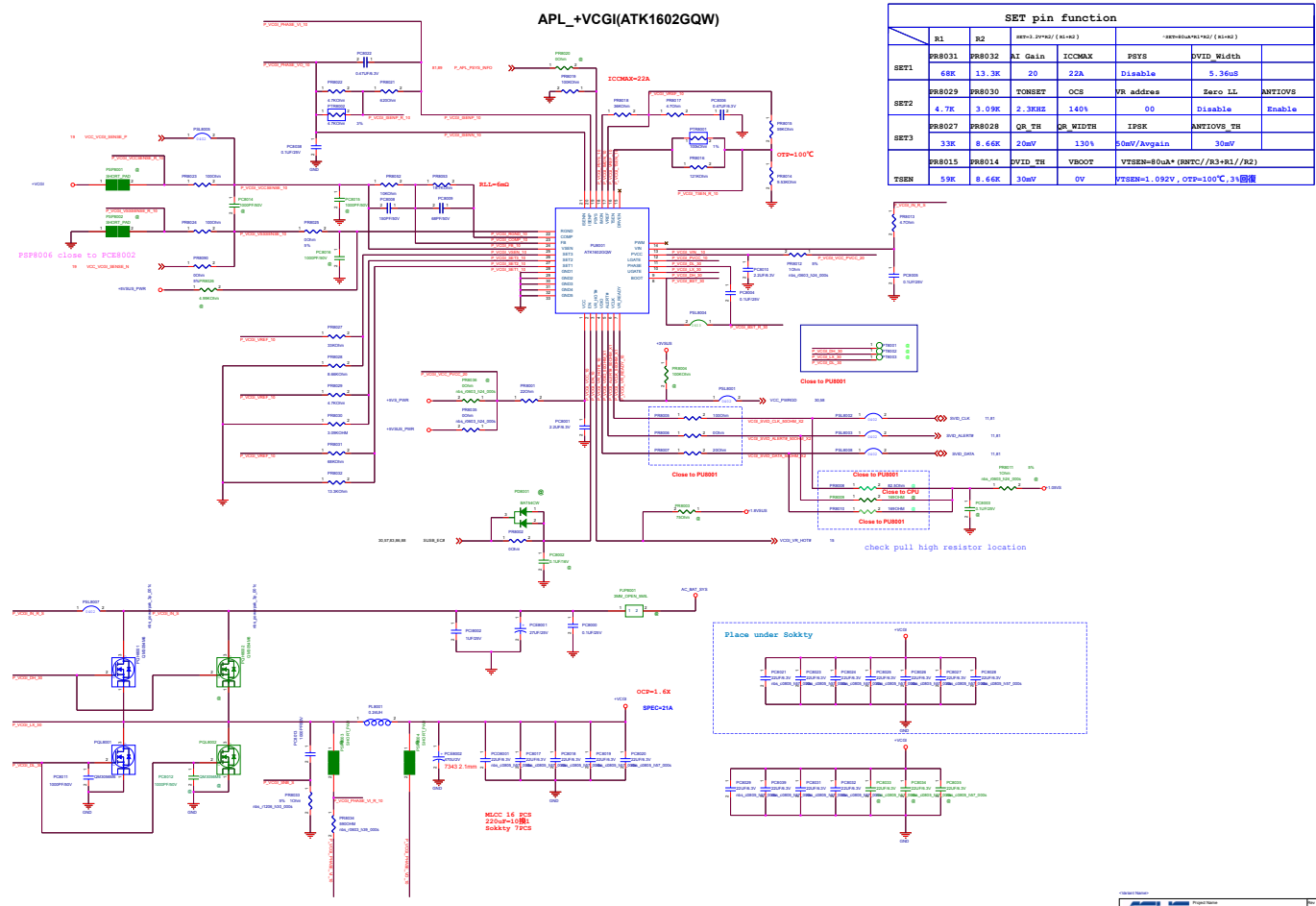
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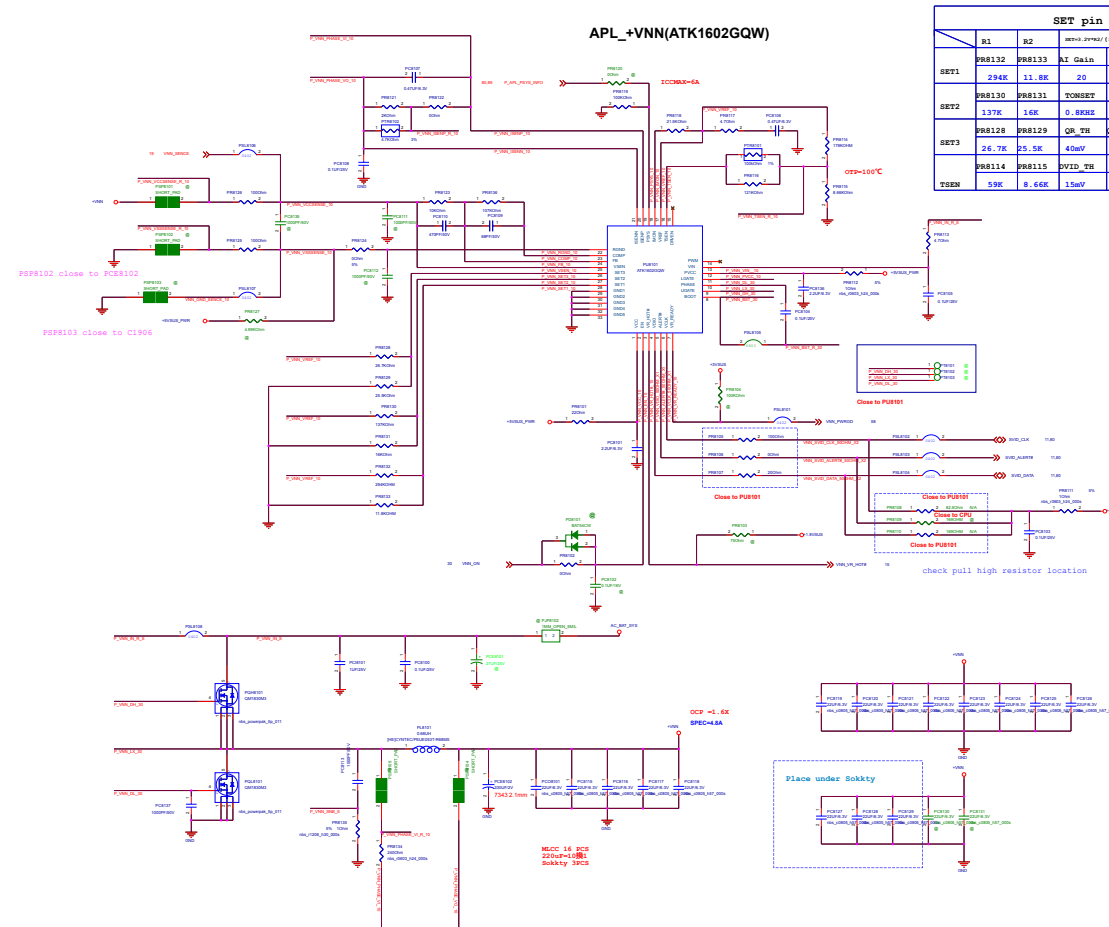
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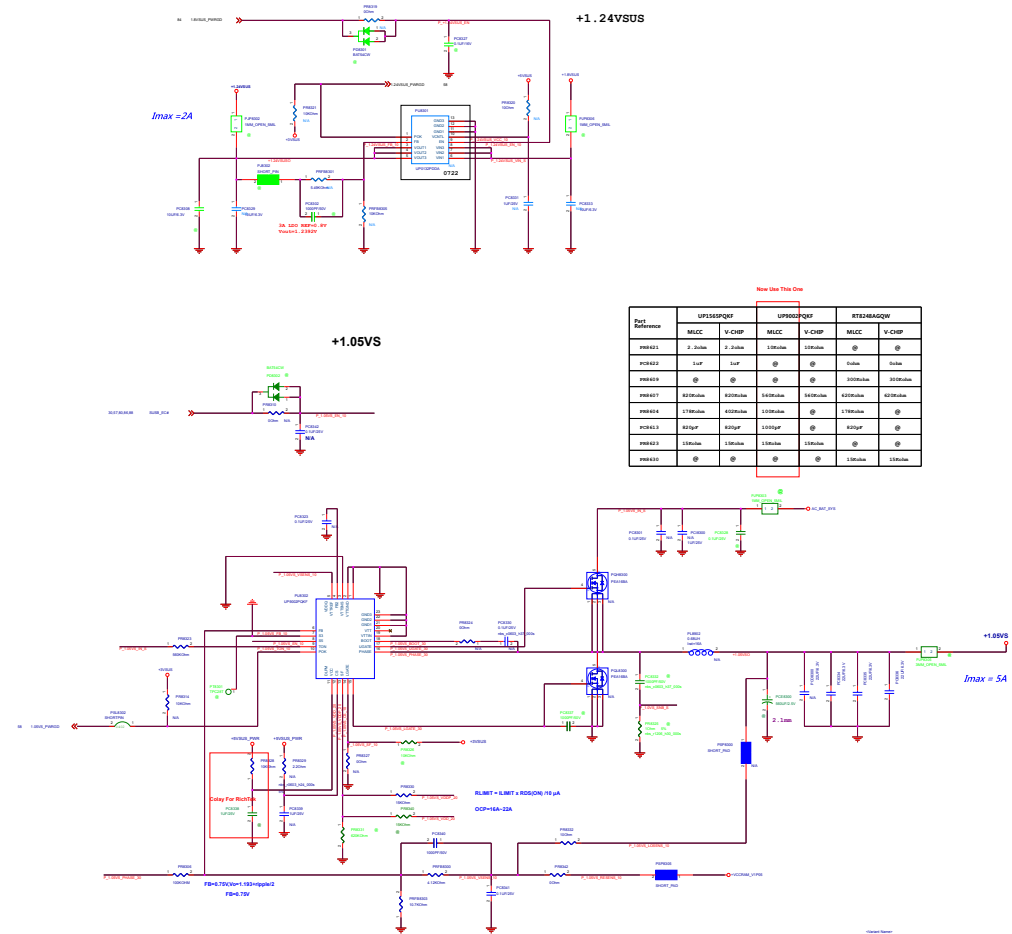
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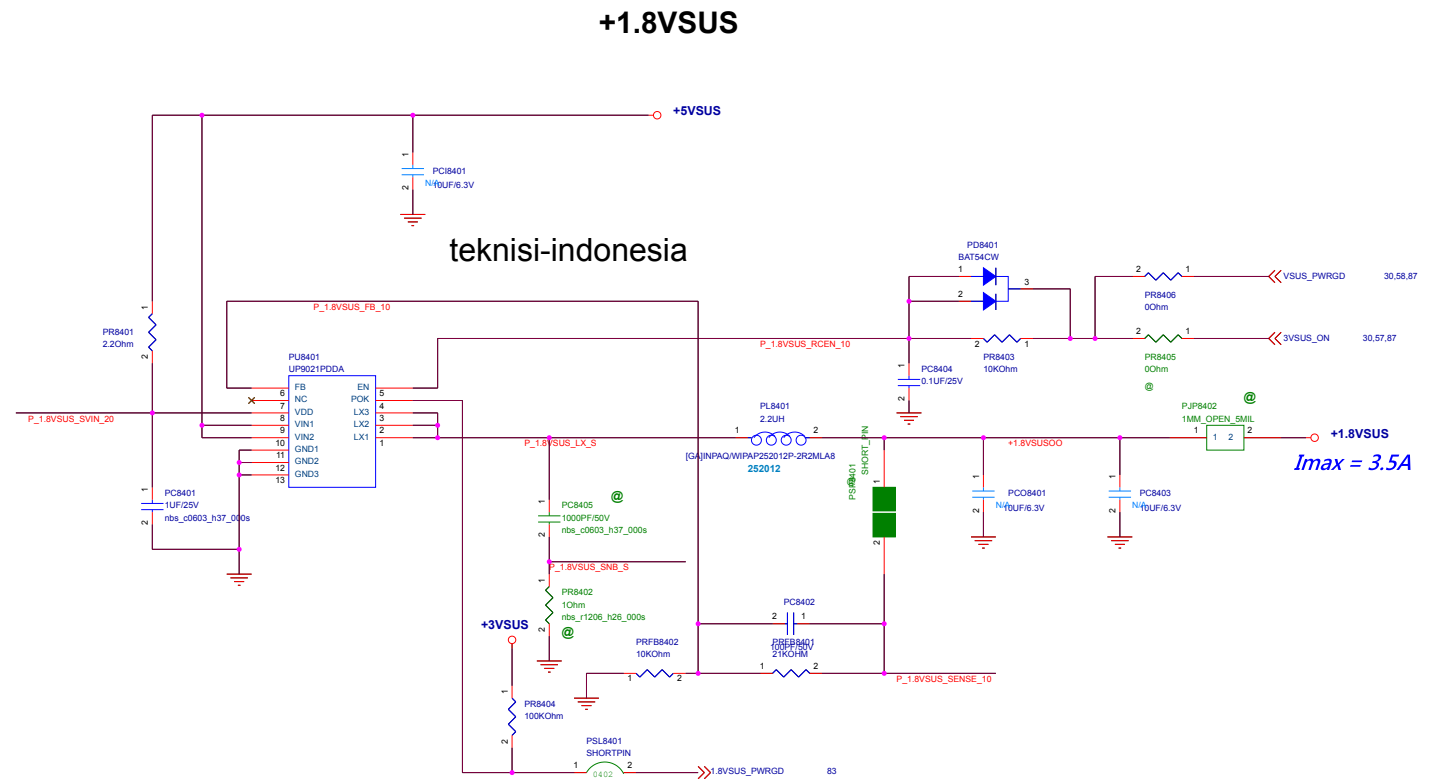
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Y

75.VGA_nVIDIA_N16V/S_ROM,XT
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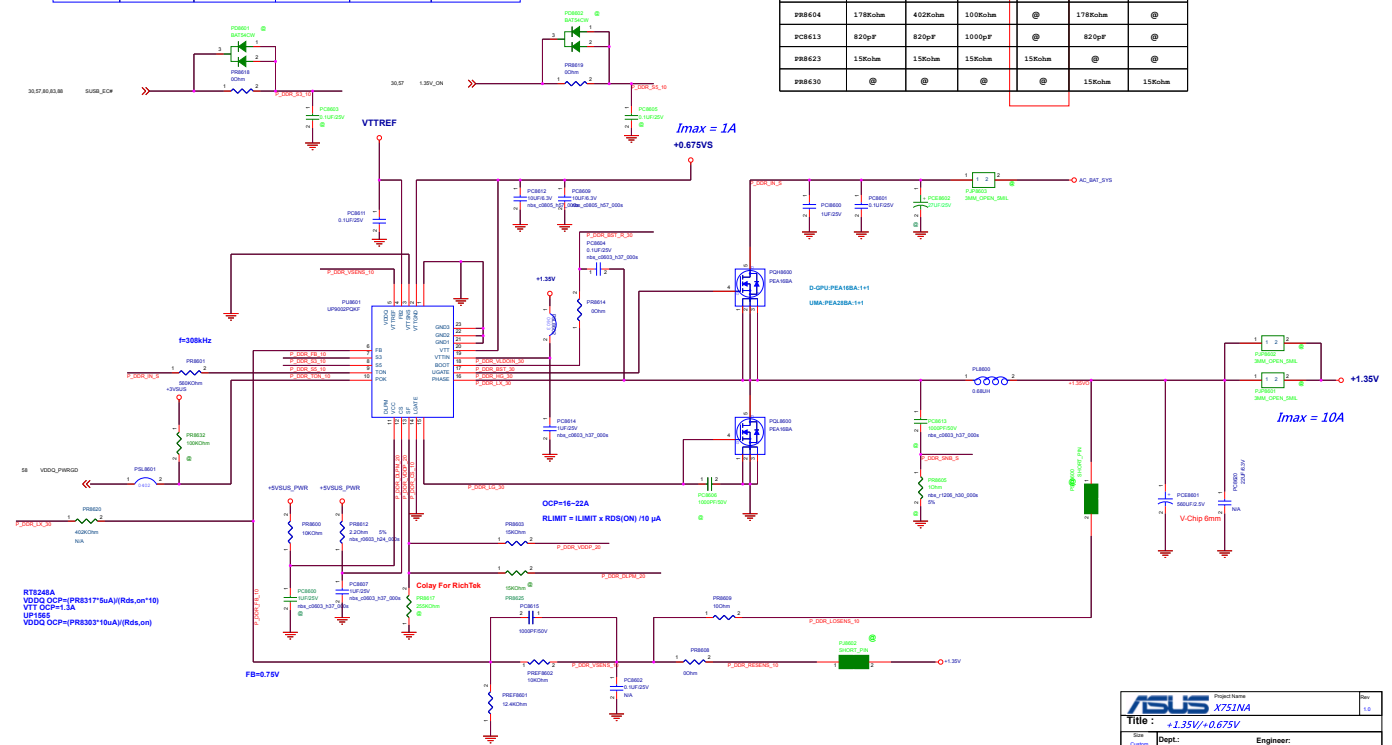
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+1.35V/+0.675VS [For DDR3L]

Now Use This One

State	Pin7(S3)	Pin8(S5)	VDDQ	VTTREF	VTT
S0	1	1	On	On	On
S3	0	1	On	On	OFF(Hi-Z)
S4/S5	0	0	OFF (Discharge)	OFF (Discharge)	OFF (Discharge)

Part Reference	UP1565PQKF		UP9062PQKF		RT8248AGQW	
	MLCC	V-CHIP	MLCC	V-CHIP	MLCC	V-CHIP
PR8621	2.2ohm	2.2ohm	10Kohm	10Kohm		
PC8622	1uF	1uF			0ohm	0ohm
PR8609	@	@			30Kohm	30Kohm
PR8607	820Kohm	820Kohm	560Kohm	560Kohm	620Kohm	620Kohm
PR8604	178Kohm	402ohm	100Kohm	@	178Kohm	@
PR8613	820pF	820pF	1000pF	@	820pF	@
PR8623	15Kohm	15Kohm	15Kohm	15Kohm		
PR8630	@	@	@	@	15Kohm	15Kohm



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Y75.VGA_nVIDIA_N16V/S_ROM,XT
AL

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83_PW_+1.24VSUS / + 1.05VSUS

84_PW_+1.8VSUS(UP9021)

85_PW_***

86_PW_+1.35V/+0.675VS(UP900
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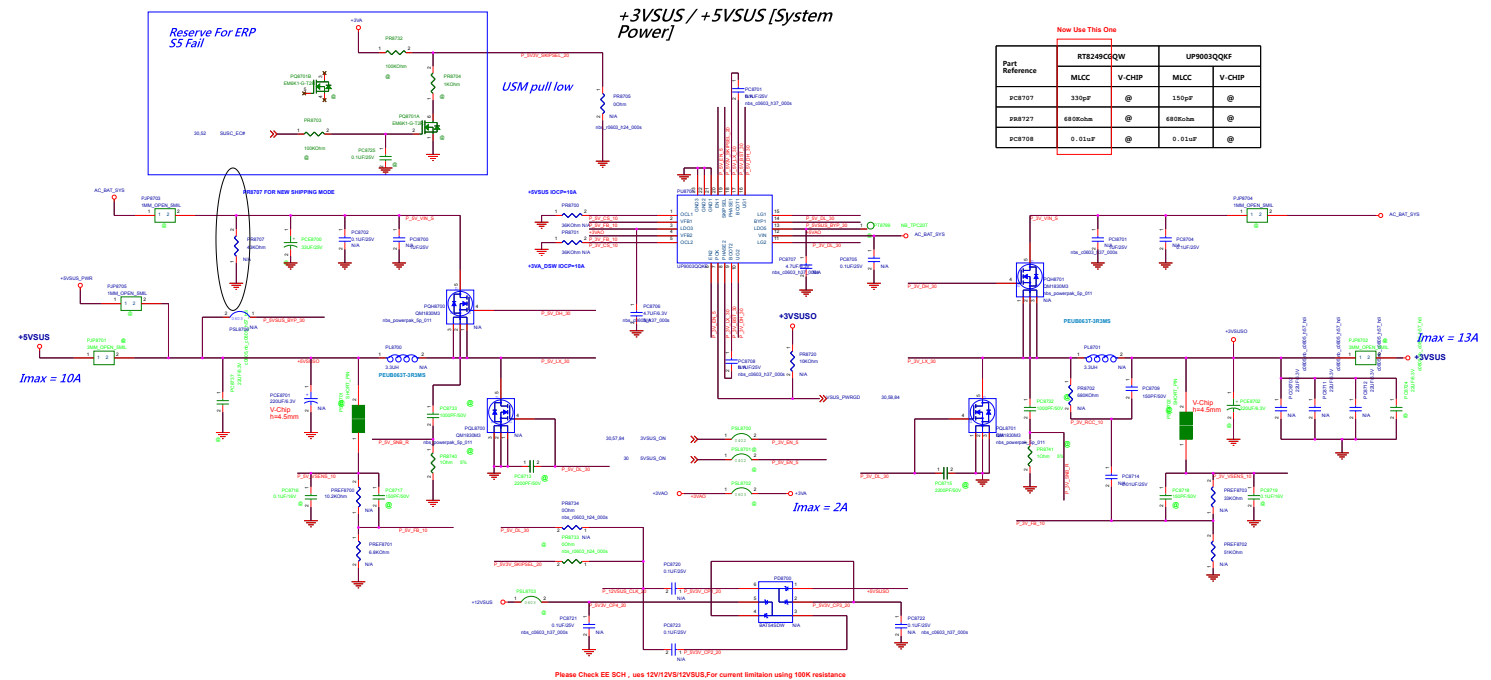
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Y

75.VGA_nVIDIA_N16V/S_ROM,XT
AL

76.VGA nVIDIA N16V/S GPIO

77.VGA nVIDIA N16V/S POWER

78.VGA ****

79.VGA ****

80_PW_APL_VCGI(ATK1602GQ
W)

81 PW APL VNN(ATK1602GQW)

82 PW ***

83 PW +1.24VSUS / + 1.05VSUS

84_PW_+1.8VSUS(UP9021)

85 PW ***

86_PW_+1.35V/+0.675VS(UP900
2)

87_PW_+3VSUS/+5VSUS (UP900
3)

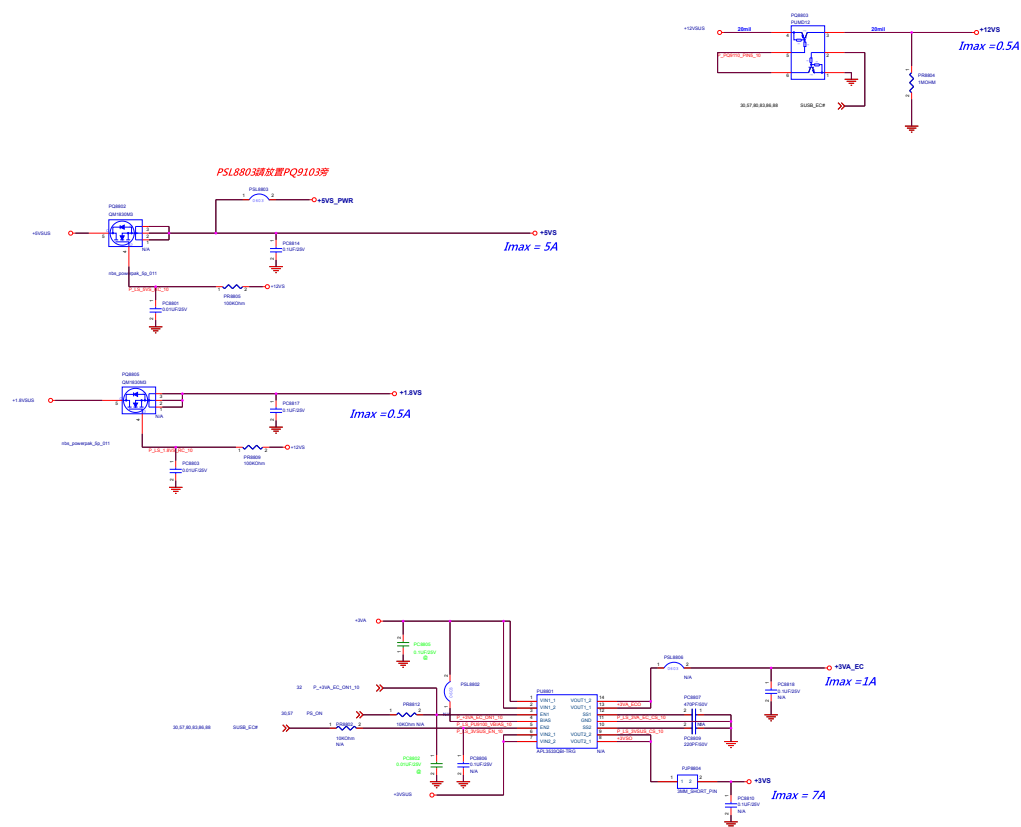
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89.PW CHARGER(SN2867RUYR)

90.PW PROTECTION

91.PW DGPU 2PHASE(uP9013)

Load Switch



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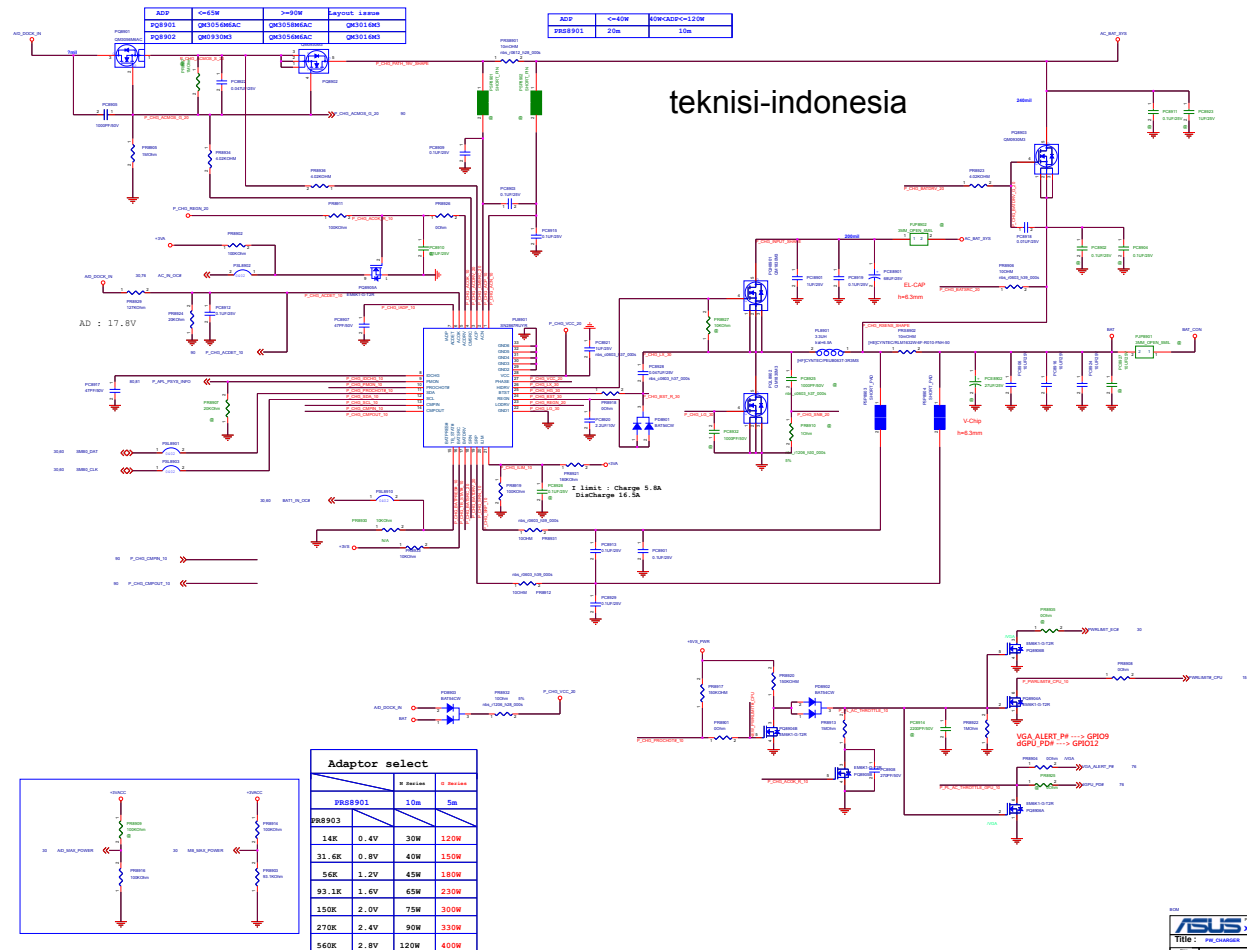
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75.VGA_nVIDIA_N16V/S_ROM,XT
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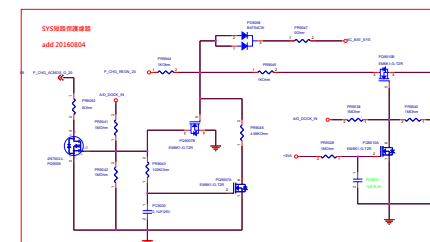
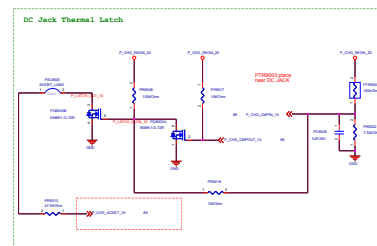
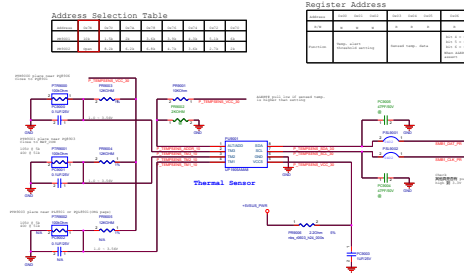
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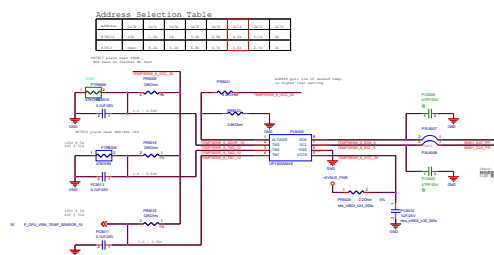
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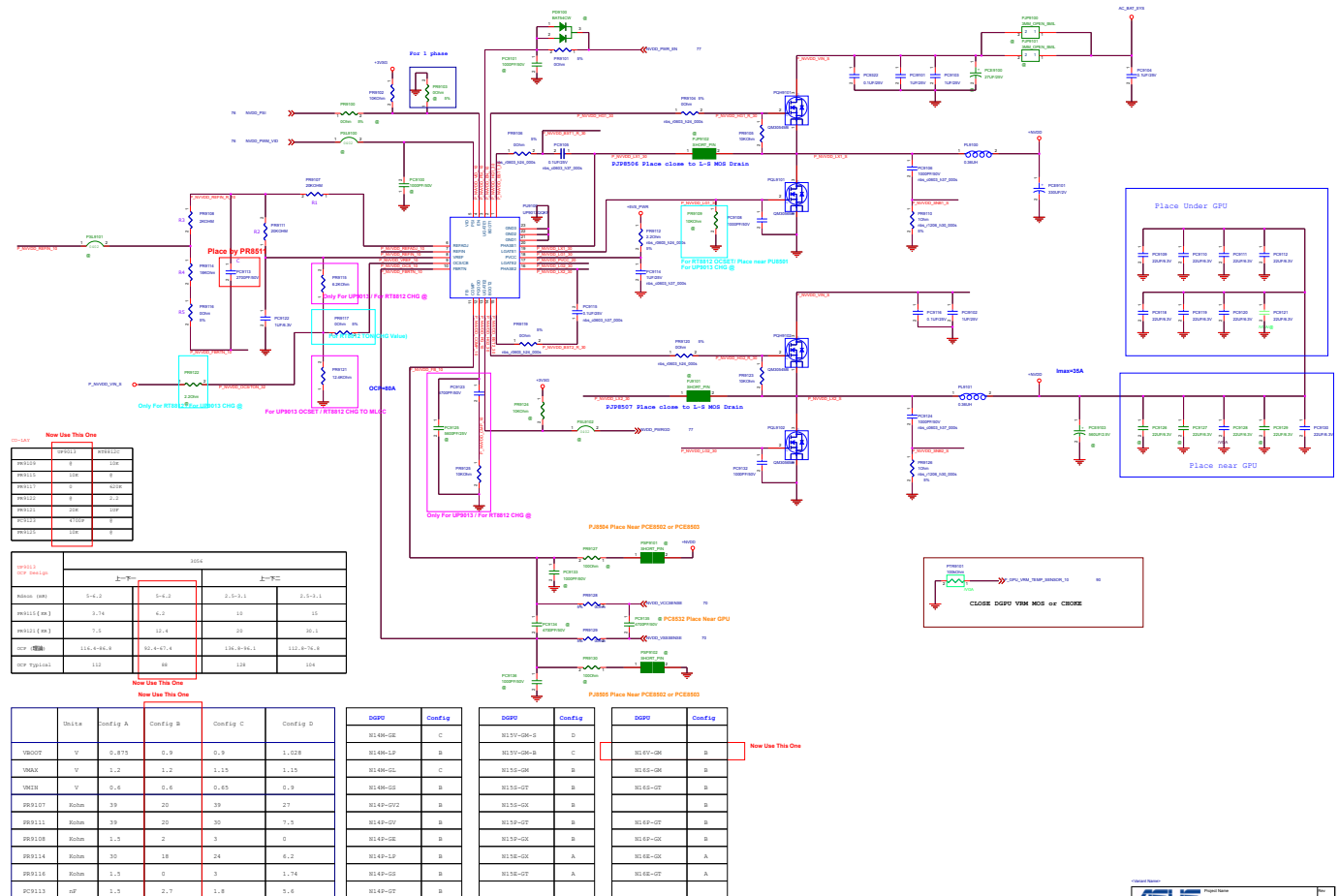
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89.PW_CHARGER(SN2867RUYR)

90.PW_PROTECTION

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